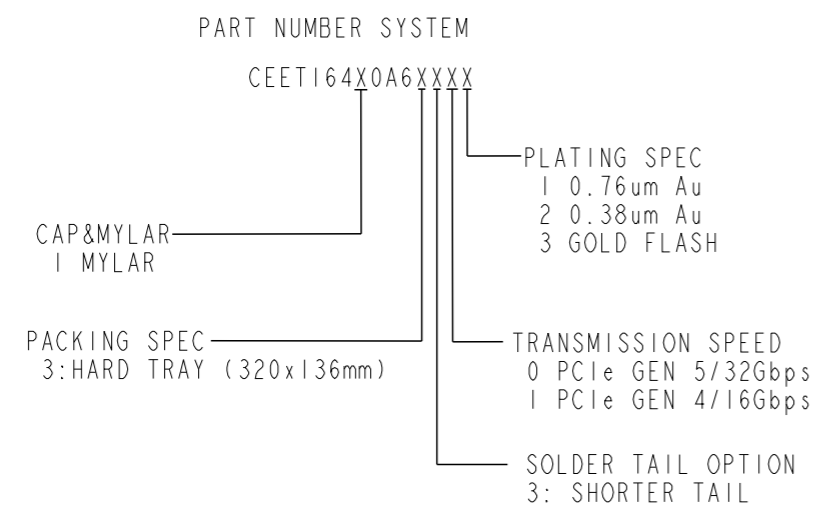
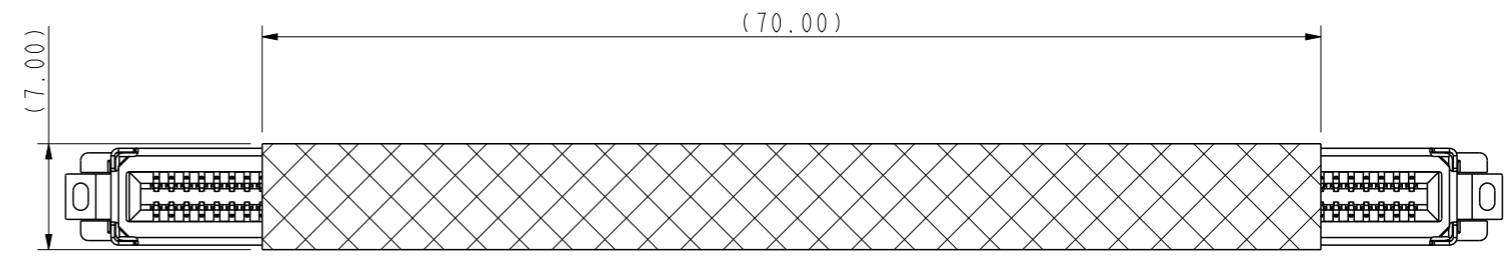
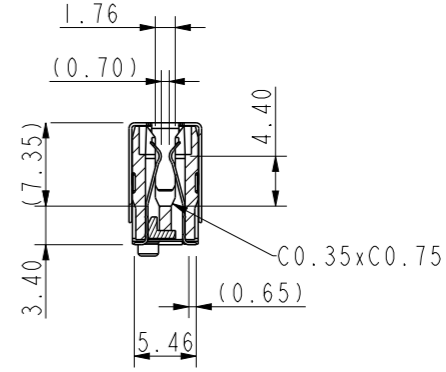
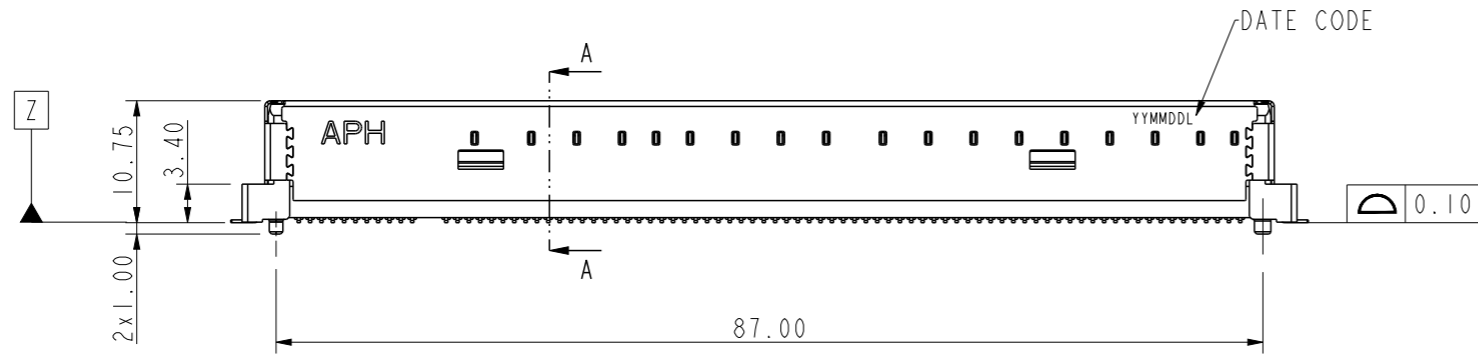
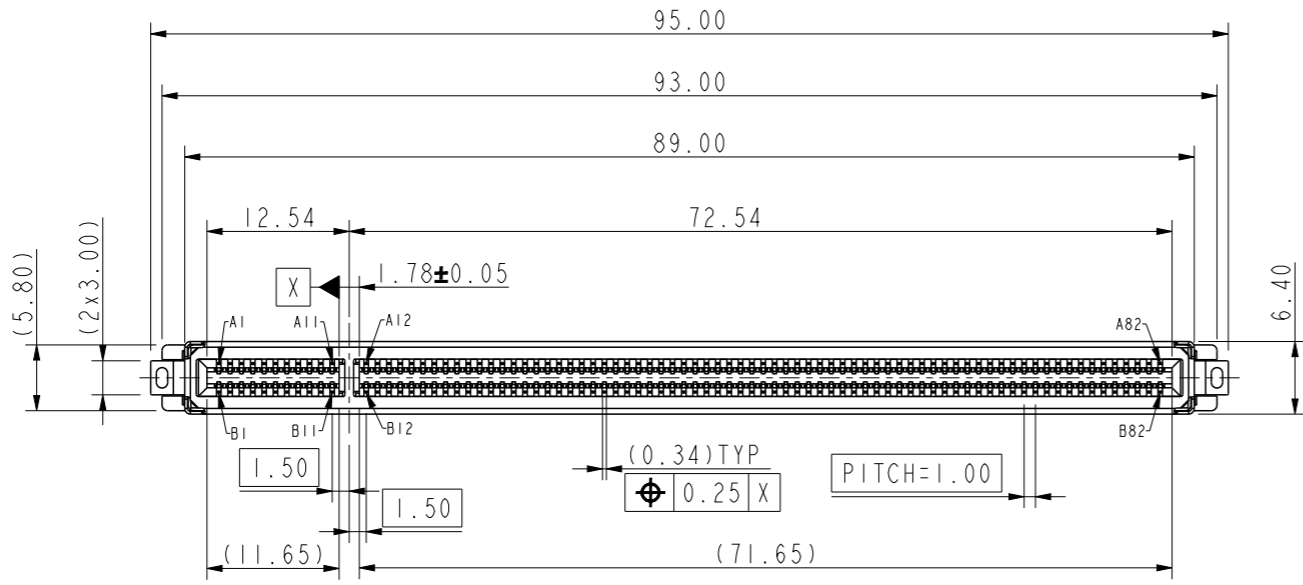


revision history				
rev	ecn no	description	dr	date
X1	-	PROPOSAL	MQ	2023/08/07
X2	-	UPDATE CAGE	MQ	2023/09/20
A	ELX-CD-F3294-1	FIRST RELEASE	MQ	2023/10/30

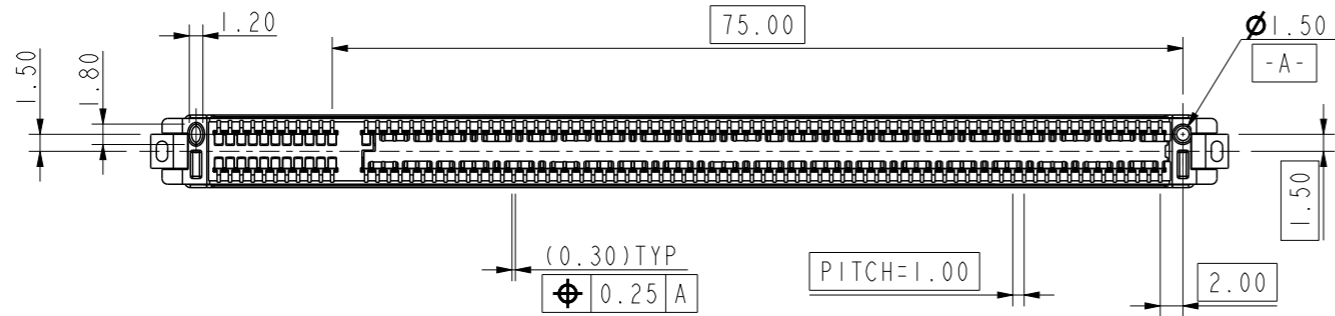
- NOTE:
- MATERIAL:
HOUSING: HIGH TEMPERATURE THERMOPLASTIC; BLACK UL94-V0;HALOGEN FREE
CONTACT: COPPER ALLOY
CAGE: SUS304
 - PLATING:
CONTACT: MATING AREA: SEE PLATING SPEC OVER 1.27-2.54 UM NICKEL UNDERPLATED
SOLDER AREA: 2.54 UM MIN TIN OVER 1.27-2.54 UM NICKEL UNDERPLATED
BOARDLOCK: 2.54UM MIN TIN OVER 1.27-2.54UM NICKEL UNDERPLATED
 - ELECTRICAL PERFORMANCE:
RATED CURRENT FOR SIGNAL PIN: 0.7A/PIN;
RATED CURRENT FOR POWER PIN: 1.1A/PIN(A2/A3/A9/A10/B1/B2/B3/B8/B10,TOTAL 9PIN).
 - MATERIAL IS COMPLIANT TO RoHS
 - TRANSMISSION SPEED:
PCIe GEN 4/16Gbps : OPEN PIN FIELD DESIGN
PCIe GEN 5/32Gbps : CONFORM TO PCIe CEM SPECIFICATION
 - MOISTURE SENSITIVITY LEVEL:1.
 - ORDERING PART NUMBER: REFER TO THE PART NUMBER SYSTEM



spec ref	dr	Mark Qiu	2023/10/20	Amphenol	mm	scale	1:1	size	A3	
tolerance std	eng	Mark Qiu	2023/11/01		ecn no					
TOLERANCES UNLESS OTHERWISE SPECIFIED	r vwr	Jun Fan	2023/10/30		amphenol-icc.com	rel level	Released			
	appr	Cat Zeng	2023/11/01		product family	PCIe COOL EDGE		sheet 1 of 5		
surface	linear	0.X	±0.25	projection	PCIe COOL EDGE CONNECTOR		cat. no.	C CEET164X0A6XXXX	rev	A
		0.XX	±0.20		1.0Pitch,X16,With CAGE,Gen4 & Gen5					
		0.XXX	±0.15							
	angular	0°	±5°							



SECTION A-A



GND PIN DEFINITION
(REFER TO PCIe CEM SPEC)

SIDE A GND PIN	A12, A15, A18, A20, A23, A24, A27, A28, A31, A34, A37, A38, A41, A42, A45, A46, A49, A51, A54, A55, A58, A59, A62, A63, A66, A67, A70, A71, A74, A75, A78, A79, A82
SIDE B GND PIN	B13, B16, B18, B21, B22, B25, B26, B29, B32, B35, B36, B39, B40, B43, B44, B47, B49, B52, B53, B56, B57, B60, B61, B64, B65, B68, B69, B72, B73, B76, B77, B80

spec ref	dr	Mark Giu	2023/10/20	<h1>Amphenol</h1>	mm	scale	1:1	size	A3	
tolerance std	eng	Mark Giu	2023/11/01		ecn no					
TOLERANCES UNLESS OTHERWISE SPECIFIED	r vwr	Jun Fan	2023/10/30		amphenol-icc.com	rel level	Released			
	appr	Cat Zeng	2023/11/01		surface	linear	0.X	±0.25	projection	PCIE COOL EDGE CONNECTOR
				angular	0°	±5°	title	PCIe COOL EDGE CONNECTOR	1.0Pitch, XI6, With CAGE, Gen4 & Gen5	
									cat. no.	C CEET164X0A6XXXX
									product family	PCIe COOL EDGE
										sheet 2 of 5

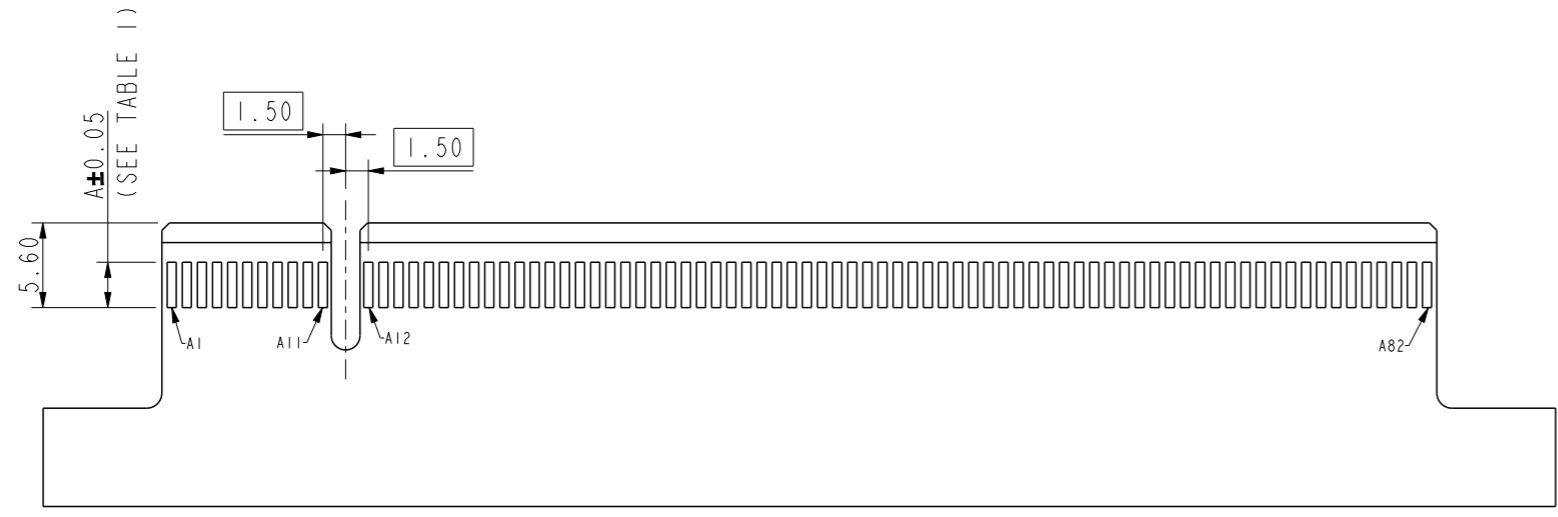
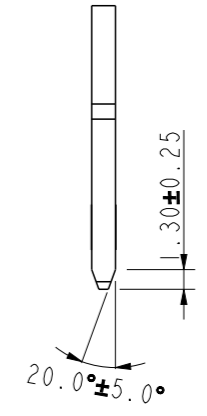
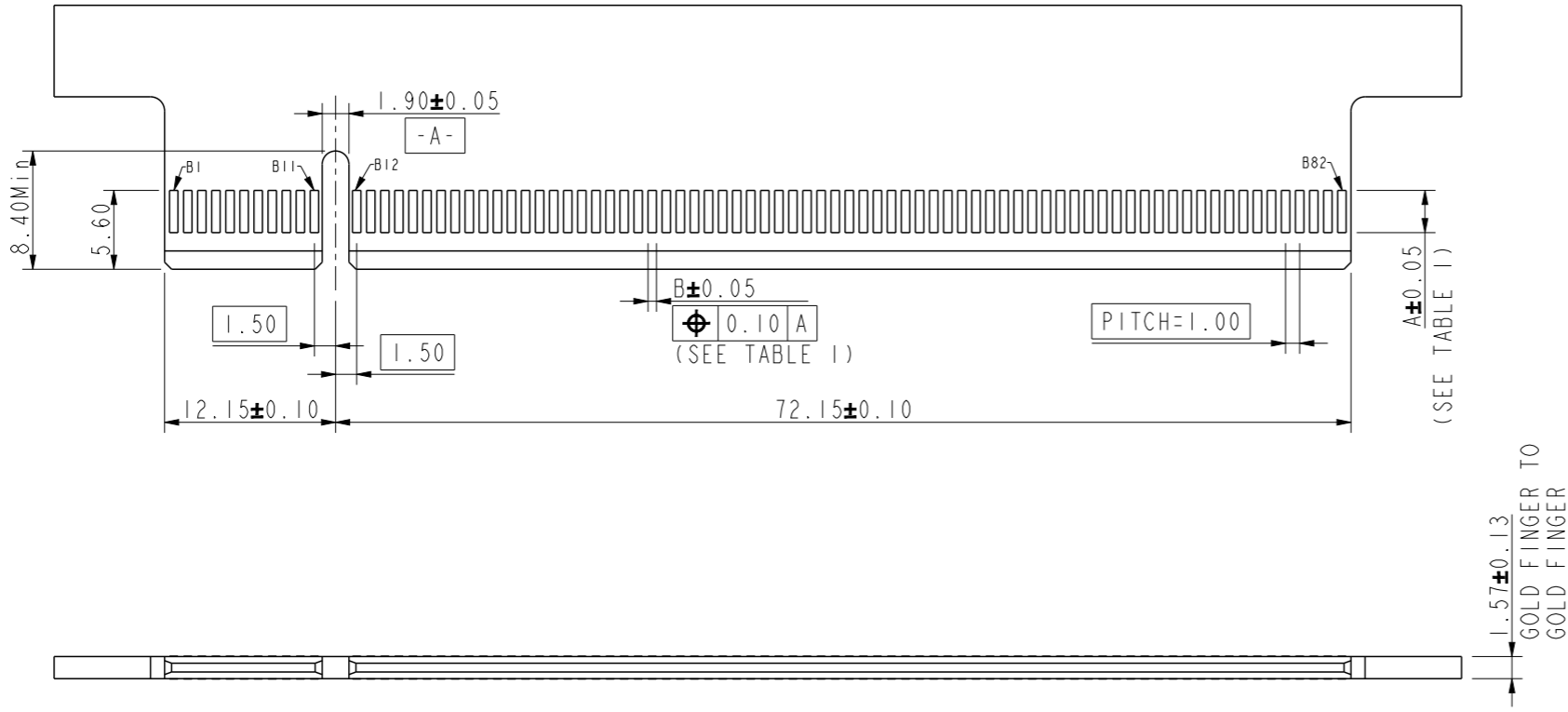


TABLE 1

ITEM/DIM	A (GOLD FINGER LENGTH)	B (GOLD FINGER WIDTH)
PCIe Gen4	3.91 ± 0.05 3.20 ± 0.05 (A1 and B81)	0.70 ± 0.05
PCIe Gen5	3.0 ± 0.05	0.60 ± 0.05

RECOMMEND LAY OUT FOR MATING CARD, THICKNESS=1.57mm
GENERAL TOLERANCE ± 0.05mm

spec ref	dr	Mark Qiu	2023/10/20
tolerance std	eng	Mark Qiu	2023/11/01
TOLERANCES UNLESS OTHERWISE SPECIFIED	rvwr	Jun Fan	2023/10/30
	appr	Cat Zeng	2023/11/01
surface	linear	0.X	±0.10
		0.XX	±0.05
		0.XXX	±0.03
	angular	0°	±5°
projection			

Amphenol

mm scale 1:1 size A3

amphenol-icc.com

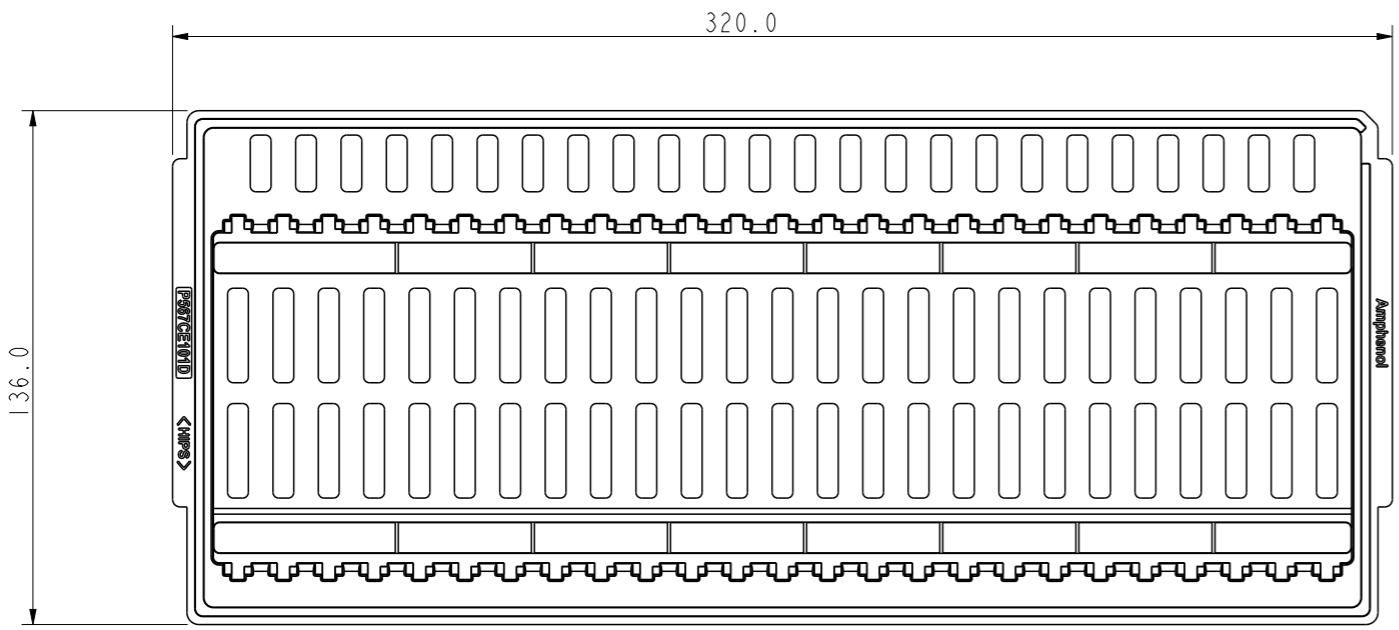
rel level Released

title PCIe COOL EDGE CONNECTOR

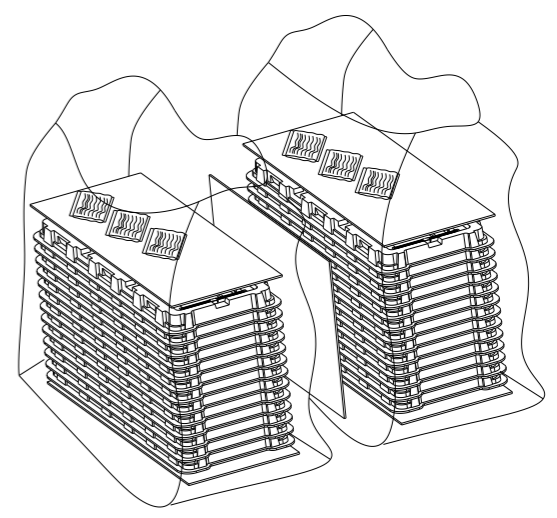
cat. no. C CEET164X0A6XXXX rev A

product family PCIe COOL EDGE

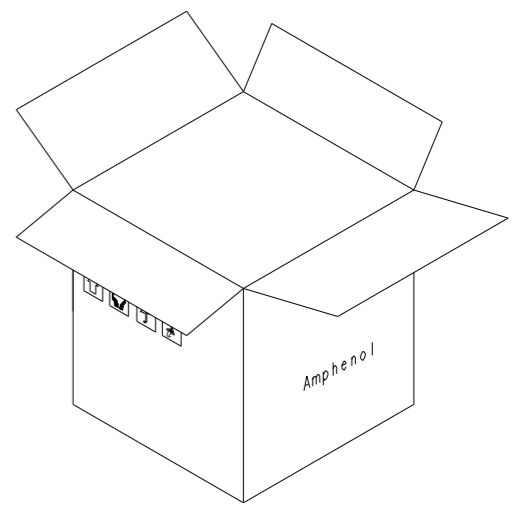
sheet 3 of 5



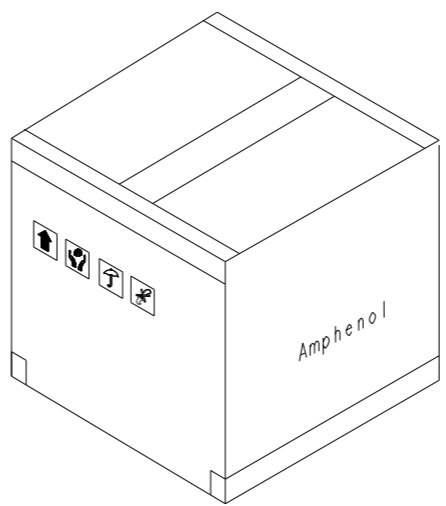
PACKING IN ALUMINIUM FOIL BAG



PUT INTO CARTON



SEALING CARTON



NOTE:
 1. TRAY MATERIAL: HIPS, BLACK, HF
 2. PACKING Q'TY: 25PCS/TRAY
 400PCS/CARTON

spec ref		dr		Mark Qiu	2023/10/20	Amphenol	mm	scale	size
tolerance std		eng		Mark Qiu	2023/11/01		1:1	A3	
TOLERANCES UNLESS OTHERWISE SPECIFIED		rvwr		Jun Fan	2023/10/30		ecn no		
		appr		Cat Zeng	2023/11/01		rel level		
surface		projection		amphenol-icc.com		Released			
✓	linear	0.X	±3.0	PCIe COOL EDGE CONNECTOR		cat. no.	C CEET164X0A6XXXX	rev	
		0.XX	±2.0	1.0Pitch, XI6, With CAGE Gen4 & Gen5				A	
	angular	0°	±5°	product family		PCIe COOL EDGE	sheet 5 of 5		