

DAC-QSFP28-100G-3M-AO

Dell DAC-QSFP28-100G-3M Compatible TAA Compliant 100GBase-CU QSFP28 Direct Attach Cable (Passive Twinax, 3m)

Features

- Up to 100 Gbps bi-directional data links
- AC coupled inputs and outputs
- Compliant with QSFP28 MSA specifications
- All-metal housing for superior EMI performance
- Single power supply 3.3V, low power consumption
- 100 Ohm differential impedance
- ROHS Compliant
- Operating Temperature: 0 to 70 Celsius



Applications

- Serial Data Transmission
- 100 Gigabit Ethernet
- Infiniband

Product Description

This is a Dell® DAC-QSFP28-100G-3M Compatible 100GBase-CU QSFP28 to QSFP28 direct attach cable that operates over passive copper with a maximum reach of 3m. It has been programmed, uniquely serialized, and data-traffic and application tested to ensure it is 100% compliant and functional. We stand behind the quality of our products and proudly offer a limited lifetime warranty. This cable is TAA (Trade Agreements Act) compliant and is built to comply with MSA (Multi-Source Agreement) standards.

AddOn's transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. – made or designated country end products."



General Specifications

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Bit Error Rate	BER			10 ⁻¹²		
Operating Temperature	Тс	0		70	°C	1
Storage Temperature	Tstg	-40		85	°C	2
Input Voltage	Vcc	3.14	3.3	3.46	V	3
Cable Impedance	Z	90	100	110	Ω	
Product Weight	GD		175	g/PCS		4
Cable Weight	GC		94	g/M		
Dust Cap Weight	GQ		1.40	g/PCS		

Notes:

- 1. Case temperature.
- 2. Ambient temperature.
- 3. For electrical power interface.
- 4. For example, the weight of a 5m cable with 26AWG is 190 + 110* (5-1) + 1.40*2=632.8g.

Cable Dimensions and Insertion Loss Level

Length	Wire Gauge AWG	Cable Diameter OD (mm)	Minimum Bending Radius R (mm)	Insertion Loss Level
3m	30AWG	6.9	35	CA-25G-S

Notes:

1. Cable insertion loss classification standard: IEEE 802.3by 110-10.

Length Tolerance

Nominal Length L1 (m)	Tolerance Range <u>+</u> (cm)		
2 < L1 ≤ 4	4		

Pin Descriptions

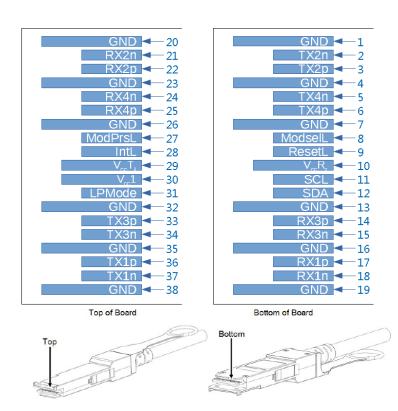
1 GND Module Ground. 2 Tx2- Transmitter Inverted Data Input. LAN2. 3 Tx2+ Transmitter Non-Inverted Data Input. LAN2. 4 GND Module Ground. 5 Tx4- Transmitter Non-Inverted Data Input. LAN4. 6 Tx4- Transmitter Non-Inverted Data Input. LAN4. 7 GND Module Ground. 5 ModSell, Module Select Pin. The module responds to 2-wire serial communication when low level. 9 Resett. Module Select Pin. The module responds to 2-wire serial communication when low level. 1 9 Resett. Module Select Pin. The module responds to 2-wire serial communication when low level. 1 1 SCL 2-Wire Serial Interface Clock. 1 2 SDA 2-Wire Serial Interface Clock. 1 3 GND Module Ground. 5 Module Ground. 7 ModPrst. The module is inserted into the indicator pin and grounded in the module. 7 ModPrst. The module is inserted into the indicator pin and grounded in the module. 7 ModPrst. The module Ground. 8 Module Ground. 9 WocTx Hansmitter Module Ground. 9 WocTx Hansmitter Module Ground.	Pin	Symbol		
2 Tx2- Transmitter Inverted Data Input. LAN2. 3 Tx2+ Transmitter Non-Inverted Data Input. LAN2. 4 GND Module Ground. 5 5 Tx4- Transmitter Inverted Data Input. LAN4. 6 6 Tx4+ Transmitter Non-Inverted Data Input. LAN4. 7 7 GND Module Ground. 5 8 ModSell. Module Select Pin. The module responds to 2-wire serial communication when low level. 1 9 Resett. Module Select Pin. The module responds to 2-wire serial communication when low level. 1 9 Resett. Module Ground. 2 10 VccRx +3.3V Receiver Power Supply. 11 SCL 2-Wire Serial Interface Clock. 12 SDA 2-Wire Serial Interface Clock. 12 SDA 2-Wire Serial Interface Data. 13 GND Module Ground. 5 14 Rx3+ Receiver Inverted Data Output. LAN3. 15 Rx3- Receiver Non-Inverted Data Output. LAN1. 18 Rx1-	1	GND	Module Ground	
3 Tx2+ Transmitter Non-Inverted Data Input. LAN2. 4 GND Module Ground. 5 5 Tx4- Transmitter Inverted Data Input. LAN4. 5 6 Tx4+ Transmitter Non-Inverted Data Input. LAN4. 5 7 GND Module Ground. 5 8 ModSell. Module Select Pin. The module responds to 2-wire serial communication when low level. 1 9 Resetl. Module Reset. 2 10 VccRx +3.3V Receiver Power Supply. 1 11 SCL 2-Wire Serial Interface Clock. 1 12 SDA 2-Wire Serial Interface Data. 1 13 GND Module Ground. 5 14 Rx3+ Receiver Non-Inverted Data Output. LAN3. 1 15 Rx3- Receiver Inverted Data Output. LAN1. 1 16 GND Module Ground. 5 17 Rx1+ Receiver Inverted Data Output. LAN1. 1 18 Rx2+ Receiver Inverted Data Output. LAN2. 2				
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18 Rx1- Receiver Inverted Data Output. LAN1. 19 GND Module Ground. 5 20 GND Module Ground. 5 Rx2- Receiver Inverted Data Output. LAN2. 22 Rx2+ Receiver Non-Inverted Data Output. LAN2. 23 GND Module Ground. 5 Ax4- Receiver Inverted Data Output. LAN4. 25 Rx4+ Receiver Non-Inverted Data Output. LAN4. 26 GND Module Ground. 5 Ax4- Receiver Non-Inverted Data Output. LAN4. 27 ModPrsL The module is inserted into the indicator pin and grounded in the module. 28 IntL Interrupt. 4 Ax4- Receiver Hoverted Data Output. LAN4. 29 VccTx +3.3V Transmitter Power Supply. 30 Vcc1 +3.3V Power Supply. 31 LPMode Low-Power Mode. 5 SAX+ Transmitter Non-Inverted Data Input. LAN3.	16	GND	Module Ground.	5
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20 GND Module Ground. 5 21 Rx2- Receiver Inverted Data Output. LAN2. 22 Rx2+ Receiver Non-Inverted Data Output. LAN2. 23 GND Module Ground. 5 24 Rx4- Receiver Inverted Data Output. LAN4. 25 Rx4+ Receiver Non-Inverted Data Output. LAN4. 26 GND Module Ground. 5 27 ModPrsL The module is inserted into the indicator pin and grounded in the module. 3 28 IntL Interrupt. 4 29 VccTx +3.3V Transmitter Power Supply. 30 Vcc1 +3.3V Power Supply. 31 LPMode Low-Power Mode. 5 32 GND Module Ground. 5 33 Tx3+ Transmitter Non-Inverted Data Input. LAN3.	18	Rx1-	Receiver Inverted Data Output. LAN1.	
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22 Rx2+ Receiver Non-Inverted Data Output. LAN2. 23 GND Module Ground. 5 24 Rx4- Receiver Inverted Data Output. LAN4. 25 Rx4+ Receiver Non-Inverted Data Output. LAN4. 26 GND Module Ground. 5 27 ModPrsL The module is inserted into the indicator pin and grounded in the module. 28 IntL Interrupt. 4 29 VccTx +3.3V Transmitter Power Supply. 30 Vcc1 +3.3V Power Supply. 31 LPMode Low-Power Mode. 5 32 GND Module Ground. 5 5 33 Tx3+ Transmitter Non-Inverted Data Input. LAN3.	20	GND	Module Ground.	5
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24 Rx4- Receiver Inverted Data Output. LAN4. 25 Rx4+ Receiver Non-Inverted Data Output. LAN4. 26 GND Module Ground. 5 ModPrsL The module is inserted into the indicator pin and grounded in the module. 3 IntL Interrupt. 4 VccTx +3.3V Transmitter Power Supply. 30 Vcc1 +3.3V Power Supply. 31 LPMode Low-Power Mode. 5 GND Module Ground. 5 5 32 GND Module Ground. 5 5	22	Rx2+	Receiver Non-Inverted Data Output. LAN2.	
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33 Tx3+ Transmitter Non-Inverted Data Input. LAN3.	31	LPMode	Low-Power Mode.	5
·	32	GND	Module Ground.	5
34 Tx3- Transmitter Inverted Data Input. LAN3.	33	Tx3+	Transmitter Non-Inverted Data Input. LAN3.	
	34	Tx3-	Transmitter Inverted Data Input. LAN3.	

35	GND	Module Ground.	5
36	Tx1+	Transmitter Non-Inverted Data Input. LAN1.	
37	Tx1-	Transmitter Inverted Data Input. LAN1.	
38	GND	Module Ground.	5

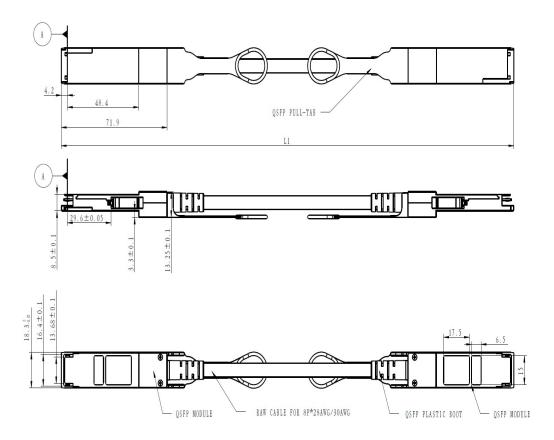
Notes:

- ModSelL is the input pin. The module responds to 2-wire serial communication commands when it is held
 "low" by the host. ModSelL allows multiple QSFP modules to be used on a single 2-wire interface bus. If
 ModSelL is "high," the module will not respond to any 2-wire interface communication from the host.
 ModSelL has internal pull-up resistors in the module.
- 2. The module restart pin, when the low level on the ResetL pin lasts longer than the minimum pulse length, resets the module and restores all user modules to their default state. When performing reset device, the host should ignore all status bits. Until the module reset interrupt is completed, please note that during hot plugging, the module will issue this information to complete the reset interrupt without resetting.
- 3. This pin is active "high," indicating that the module is running under a low-power module.
- 4. IntL is the output pin, which is the open collector output and must be pulled up to the Vcc on the motherboard. When it is "low," it indicates that the module may malfunction. The host uses a 2-wire serial interface to identify the interrupt source.
- 5. The circuit ground is internally isolated from the chassis ground.

Electrical Pad Layout



Mechanical Specifications



All Dimensions are ± 0.2 mm Unless Otherwise Specified. Unit: mm

About AddOn Networks

In 1999, AddOn Networks entered the market with a single product. Our founders fulfilled a severe shortage for compatible, cost-effective optical transceivers that compete at the same performance levels as leading OEM manufacturers. Adhering to the idea of redefining service and product quality not previously had in the fiber optic networking industry, AddOn invested resources in solution design, production, fulfillment, and global support.

Combining one of the most extensive and stringent testing processes in the industry, an exceptional free tech support center, and a consistent roll-out of innovative technologies, AddOn has continually set industry standards of quality and reliability throughout its history.

Reliability is the cornerstone of any optical fiber network and is in engrained in AddOn's DNA. It has played a key role in nurturing the long-term relationships developed over the years with customers. AddOn remains committed to exceeding industry standards with certifications from ranging from NEBS Level 3 to ISO 9001:2005 with every new development while maintaining the signature reliability of its products.













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