

XCVR-A00CRJ-OPC

Ciena XCVR-A00CRJ Compatible 1000Base-TX SFP Transceiver (Copper, 100m, RJ-45, -40 to 85C)

Features

- INF-8074 Compliance
- RJ-45 Connector
- Copper Media Type
- Support for LOS indication function configuration
- Industrial Temperature -40 to 85 Celsius
- Hot Pluggable
- Metal with Lower EMI
- Excellent ESD Protection
- RoHS Compliant and Lead Free



Applications:

- 1000Base Ethernet
- Access and Enterprise

Product Description

This Ciena XCVR-A00CRJ compatible SFP transceiver provides 10/100/1000Base-TX throughput up to 100m over a copper connection via a RJ-45 connector. This TX module supports 10/100/1000Base auto-negotiation and can be configured to fit your needs. It is guaranteed to be 100% compatible with the equivalent Ciena transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. It is built to meet or exceed the specifications of Ciena, as well as to comply with MSA (Multi-Source Agreement) standards to ensure seamless network integration. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

OptioConnect's transceivers are RoHS compliant and lead-free.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Maximum Supply Voltage	Vcc	-0.5		4.0	V	
Storage Temperature	Tstg	-40		85	°C	1
Operating Case Temperature	Тс	-40	25	85	°C	1
Relative Humidity	RH	5		95	%	
Data Rate (Auto Working Mode)			1000		Mbps	
Data Rate (SGMII Working Mode)			10/100/1000		Mbps	
Data Rate (Full Working Mode)			1000		Mbps	

Notes:

- 1. Ambient temperature.
- 2. Case temperature.

Electrical Specifications

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Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Supply Voltage	Vcc	3.135	3.3	3.465	V	
Module Supply Current	Icc			400	mA	
Transmitter Differential Input Voltage (TD +/-)		500		2400	mVp-p	1
Receiver Differential Output Voltage (RD +/-)		500		2000	mVp-p	2
Differential Input Impedance		80	100	120	Ω	
Low-Speed Output:	VOH	2.0		Vcc	V	3
Transmitter Fault (Tx_Fault)/Loss of Signal (LOS)	VOL	0		0.8	V	
Low-Speed Input:	VIH	2.0		Vcc	V	4
Transmitter Disable (Tx_Disable), MOD_DEF 1, MOD_DEF 2	VIL	0		0.8	V	
Data Output Rise/Fall Time	Tr/Tf		180		ps	

Notes:

- 1. Internally AC coupled and terminated to 100Ω differential load.
- 2. Internally AC coupled but requires a 100Ω differential termination or internal to Serializer/Deserializer.
- 3. Pulled up externally with a $4.7k\Omega-10k\Omega$ resistor on the host board to VccT/R.
- 4. MOD_DEF1 and MOD_DEF2 must be pulled up externally with a $4.7k\Omega$ - $10k\Omega$ resistor on the host board to VccT/R.

Pin Descriptions

Pin	Symbol	Name/Description	Note
1	VeeT	Transmitter Ground.	
2	Tx_Fault	Transmitter Fault Indication.	1
3	Tx_Disable	Transmitter Disable - Module disables on "high" or "open."	2
4	MOD_DEF2	Module Definition 2 - 2-Wire Serial ID Interface.	3
5	MOD_DEF1	Module Definition 1 - 2-Wire Serial ID Interface.	3
6	MOD_DEF0	Module Definition 0 - Grounded in the module.	3
7	Rate Select	Not Connected.	
8	LOS	Loss of Signal.	4
9	VeeR	Receiver Ground.	
10	VeeR	Receiver Ground.	
11	VeeR	Receiver Ground.	
12	RD-	Inverse Received Data Out.	5
13	RD+	Received Data Out.	5
14	VeeR	Receiver Ground.	
15	VccR	Receiver Power - +3.3V±5%.	6
16	VccT	Transmitter Power - +3.3 V±5%.	6
17	VeeT	Transmitter Ground.	
18	TD+	Transmitter Data In.	7
19	TD-	Inverse Transmitter Data In.	7
20	VeeT	Transmitter Ground.	

Notes:

- 1. Tx_Fault is an open collector/drain output which should be pulled up externally with a $4.7k\Omega-10k\Omega$ resistor on the host board to supply <VccT+0.3V or VccR+0.3V. When "high," this output indicates a laser fault of some kind. "Low" indicates normal operation. In the low state, the output will be pulled to <0.8V.
- 2. Tx_Disable input is used to shut down the laser output per the state table below. It is pulled up within the module with a $4.7k\Omega-10k\Omega$ resistor.

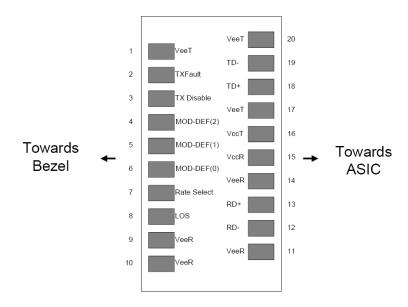
Low (0-0.8V): Transmitter On Between (0.8V and 2V): Undefined

High (2.0VccT): Transmitter Disabled Open: Transmitter Disabled

- 3. MOD_DEF 0, 1, 2. These are the module definition pins. They should be pulled up with a $4.7k\Omega-10k\Omega$ resistor on the host board to supply less than VccT+0.3V or VccR+0.3V.
 - MOD DEF 0 is grounded by the module to indicate that the module is present.
 - MOD_DEF 1 is clock line of 2-wire serial interface for optional serial ID.
 - MOD DEF 2 is data line of 2-wire serial interface for optional serial ID.
- 4. Support for LOS indication function configuration.
- 5. RD-/+: These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω differential at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board.

- 6. VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V±5% at the SFP connector pin. The in-rush current will typically be no more than 30mA above steady state supply current after 500ns.
- 7. TD-/+: These are the differential transmitter inputs. They are AC coupled differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on host board.

Electrical Pad Layout

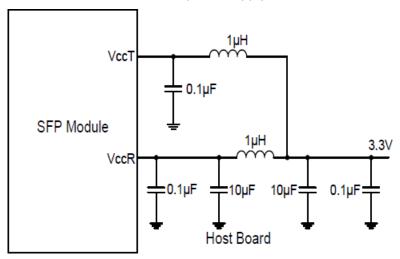


Required Host Board Components

The MSA power supply noise rejection filter is required on the host PCB to meet data sheet performance. The MSA filter incorporates an inductor which should be rated 400mA DC and 1Ω serial resistance or better. It should not be replaced with a ferrite. The required filter is illustrated in figure below.

The MSA also specifies that $4.7k\Omega-10k\Omega$ pull-up resistors for Tx_Fault, LOS, and MOD_DEF 0, 1, 2 are required on the host PCB. The figure below is the suggested transceiver/host interface.

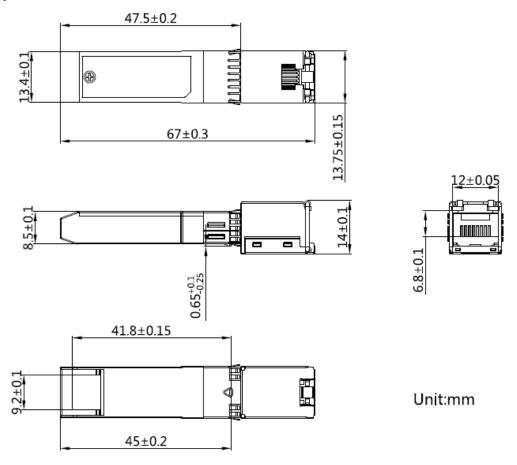
The figure below shows the recommended host board power supply circuit.



Recommended Application Interface Circuit



Mechanical Specifications



OptioConnect

Innovation for the Future of High-Speed Networking

Who We Are

OptioConnect is reshaping the landscape of communication and high-speed networking through intelligent technology. With a core focus on cutting edge technology, we deliver smarter fiber optic solutions for enterprise networks, data centers, and next-gen telecom infrastructures.

What We Do

At OptioConnect, we fuse advanced engineering with intelligent automation to drive the future of networking. Our Al-integrated solutions are designed to optimize performance and streamline operations with:

- Superior Performance
- Network and traffic optimization
- Intelligent energy management
- Seamless OEM compatibility
- Scalable cost-efficiency

Smarter Networks by Design

Innovation isn't just a goal—it's our process. We embed AI and machine learning across our R&D and product lines, enabling adaptive performance, automated tuning, and faster deployment cycles. The result? Networks that don't just work—they learn, evolve, and outperform.

Our Team

Our engineers, data scientists, and network architects bring decades of experience and a future-focused mindset. We provide hands-on support with intelligent insights that turn complex challenges into simple solutions.

Our Mission

To deliver AI-enhanced connectivity that reduces cost, increases speed, and maximizes efficiency—empowering our partners to operate at the forefront of a rapidly evolving digital world.

Let's Connect

Discover how OptioConnect's intelligent infrastructure solutions can power your network's next leap forward. www.optioconnect.com | info@optioconnect.com







