

MGBIC-BX120-D-OPC

Enterasys® MGBIC-BX120-D Compatible TAA 1000Base-BX SFP Transceiver (SMF, 1550nmTx/1490nmRx, 120km, LC, DOM)

Features

- INF-8074 and SFF-8472 Compliance
- Simplex LC Connector
- Single-mode Fiber
- Commercial Temperature 0 to 70 Celsius
- Hot Pluggable
- Metal with Lower EMI
- Excellent ESD Protection
- RoHS Compliant and Lead Free



Applications:

- 1000Base-BX Ethernet
- 1x Fibre Channel
- Access (FTTx) and Enterprise

Product Description

This Enterasys® MGBIC-BX120-D compatible SFP transceiver provides 1000Base-BX throughput up to 120km over single-mode fiber (SMF) using a wavelength of 1550nmTx/1490nmRx via an LC connector. This bidirectional unit must be used with another transceiver or network appliance of complementing wavelengths. It can operate at temperatures between 0 and 70C. Our transceiver is built to meet or exceed OEM specifications and is guaranteed to be 100% compatible with Enterasys®. It has been programmed, uniquely serialized, and tested for data-traffic and application to ensure that it will initialize and perform identically. All of our transceivers comply with Multi-Source Agreement (MSA) standards to provide seamless network integration. Additional product features include Digital Optical Monitoring (DOM) support which allows access to real-time operating parameters. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

Regulatory Compliance

- ESD to the Electrical PINs: compatible with MIL-STD-883E Method 3015.4
- ESD to the LC Receptacle: compatible with IEC 61000-4-3
- EMI/EMC compatible with FCC Part 15 Subpart B Rules, EN55022:2010
- Laser Eye Safety compatible with FDA 21CFR, EN60950-1& EN (IEC) 60825-1,2
- RoHS compliant with EU RoHS 2.0 directive 2015/863/EU

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Maximum Supply Voltage	V _{CC}	-0.5	3.6	V
Storage Temperature	T _S	-40	+85	°C
Operating Case Temperature	T _C	0	+70	°C
Operating Humidity	RH	5	85	%
Receiver Power	R _{MAX}		-6	dBm
Maximum Bitrate	B _{max}		1.25	Gbps

Electrical Characteristics (TOP=25°C, V_{CC}=3.3Volts)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Power Supply Voltage	V _{CC}	3.15	3.30	3.47	V	
Power Supply Current	I _{CC}			300	mA	
Transmitter						
Differential data input swing	V _{in}	400		1800	mVpp	1
Input differential impedance	Z _{in}	90	100	110	Ω	2
Receiver						
Differential data output swing	V _{out}	400		2000	mVpp	1
Output differential impedance	Z _{in}	90	100	110	Ω	

Notes:

1. LVPECL logic, internally AC coupled
2. R_{in} > 100 kohms @ DC

Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Transmitter						
Optical Power (average)	Pout	0		5	dBm	1
Optical Extinction Ratio	ER	9			dB	2
Center Wavelength	λC	1530	1550	1570	nm	
Spectral Width (-20dB)	Δλ			1	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Rise/Fall Time (20% ~ 80%)	tr/tf			0.26	ns	
Total Jitter	TJ			0.43	UI	
Pout@TX Disable Asserted	Pout			-35	dBm	
Output Optical Eye	Compliant with IEEE 802.3					2
Receiver						
Center Wavelength	λC	1470	1490	1510	nm	
Receiver Sensitivity (average)	Pmin			-32	dBm	3
Receiver overload	Pmax	-6			dBm	4
LOS De-Assert	LOSD			-33	dBm	
LOS Assert	LOSA	-40			dBm	
LOS Hysteresis		1			dB	3

Notes:

1. Coupled into a Single-mode fibre
2. Filtered, measured with a PRBS 2⁷ -1 test pattern @1250Mbps
3. Minimum average optical power is measured at BER less than 1E-12, with 2⁷ -1 PRBS and ER=9 dB
4. Exceeding the Receiver overload can physically damage the module. Please use appropriate attenuation.

Pin Descriptions

Pin	Symbol	Name/Descriptions	Ref.
1	VeeT	Transmitter Ground (Common with Receiver Ground).	1
2	TX Fault	Transmitter Fault. LVTTTL-O	2
3	TX Disable	Transmitter Disable. Laser output disabled on high or open. LVTTTL-I.	3
4	SDA	2-Wire Serial Interface Data Line (Same as MOD-DEF2 in INF-8074i). LVTTTL-I/O.	
5	SCL	2-Wire Serial Interface Data Line (Same as MOD-DEF2 in INF-8074i). LVTTTL-I.	
6	MOD_ABS	Module Absent, Connect to VeeT or VeeR in Module.	4
7	RS0	Rate Select 0. Not used	5
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation. LVTTTL-O.	2
9	RS1	Rate Select 1. Not used	5
10	VeeR	Receiver Ground (Common with Transmitter Ground).	1
11	VeeR	Receiver Ground (Common with Transmitter Ground).	1
12	RD-	Receiver Inverted DATA out. AC Coupled. CML-O.	
13	RD+	Receiver Non-inverted DATA out. AC Coupled. CML-O.	
14	VeeR	Receiver Ground (Common with Transmitter Ground).	1
15	VccR	Receiver Power Supply.	
16	VccT	Transmitter Power Supply.	
17	VeeT	Transmitter Ground (Common with Receiver Ground).	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled. CML-I.	
19	TD-	Transmitter Inverted DATA in. AC Coupled. CML-O.	
20	VeeT	Transmitter Ground (Common with Receiver Ground).	1

Notes:

1. The module signal ground contacts, VeeR and VeeT, should be isolated from the module case.
2. This contact is an open collector/drain output and should be pulled up to the Vcc_Host with resistor in the range 4.7KΩ to 10KΩ. Pull ups can be connected to one or several power supplies, however the host board design shall ensure that no module contract has voltage exceeding module VccT/R +0.5.V.
3. Tx_Disable is an input contact with a 4.7KΩ to 10KΩ pull-up resistor to VccT inside module.
4. Mod_ABS is connected to VeeT or VeeR in the SFP+ module. The host may pull the contract up to Vcc_Host with a resistor in the range from 4.7KΩ to 10KΩ. Mod_ABS is asserted "High" when the SFP+ module is physically absent from a host slot.
5. Internally pulled down per SFF-8431



Pin-out of connector Block on Host board

Recommended Circuit Schematic



Mechanical Specifications

Small Form Factor Pluggable (SFP) transceivers are compatible with the dimensions defined by the SFP Multi-Sourcing Agreement (MSA).



EEPROM Information

EEPROM memory map specific data field description is as below:

2 wire address 1010000X (A0h)	2 wire address 1010001X (A2h)
0	0
Serial ID Defined by SFP MSA (96 bytes)	Alarm and Warning Thresholds (56 bytes)
95	55
Vendor Specific (32 bytes)	Cal Constants (40 bytes)
127	95
Reserved, SFF8079 (128 bytes)	Real Time Diagnostic Interface (24 bytes)
	119
	Vendor Specific (8 bytes)
	127
	User Writable EEPROM (120 bytes)
	247
	Vendor Specific (8 bytes)
255	255