LPe16100-OPT-OPC

Emulex® LPe16100-OPT Compatible TAA 16GBase-SW Fibre Channel SFP+ Transceiver (MMF, 850nm, 300m, LC)

Features

- • SFF-8432 and SFF-8472 Compliance
- • Duplex LC Connector
- • Multi-mode Fiber
- • Commercial Temperature 0 to 70 Celsius
- • Hot Pluggable
- • Metal with Lower EMI
- • Excellent ESD Protection
- • RoHS Compliant and Lead Free

Applications:

- • Fibre Channel
- • Access and Enterprise

Product Description

This Emulex® LPe16100-OPT compatible SFP+ transceiver provides 16GBase-SW Fibre Channel throughput up to 300m over multi-mode fiber (MMF) using a wavelength of 850nm via an LC connector. It is guaranteed to be 100% compatible with the equivalent Emulex® transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. It is built to meet or exceed the specifications of Emulex, as well as to comply with MSA (Multi-Source Agreement) standards to ensure seamless network integration. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Maximum Supply Voltage	Vcc	-0.5	4.0	V
Storage Temperature	TS	-40	85	°C
Operating Case Temperature	Tc	-5	70	°C
Operating Humidity	RH	5	95	%
50/125µm G.652 MMF (OM3)	Lmax		300	m

Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Power Supply Voltage	Vcc	3.13	3.30	3.47	V	
Power Supply Current	Icc			300	mA	
Transmitter						
Differential data input swing	Vin,pp	120	600	850	mV	
Input differential impedance	Zin	90	100	110	Ω	
TX Disable-High		2.0		Vcc+0.3	V	
TX Disable-Low		Vee-0.3		0.8	V	
TX Fault-High		2.0		Vcc+0.3	V	
TX Fault-Low		Vee-0.3		0.8	V	
Receiver						
Differential data output swing	Vout, pp	300	600	850	mV	
Output differential impedance	Zin	90	100	110	Ω	
LOS-High		2.0		Vcc+0.3	V	
LOS-Low		Vee-0.3		0.8	V	

Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Transmitter						
Output Optical Power (average)	P_{AVE}	-7.3		-1	dBm	
Optical Modulation Amplitude	P(OMA)	-4.3	-2		dBm	
Extinction Ratio	ER	3			dB	
Transmitter and Dispersion Penalty	TDP			3.9	dB	
Average Launch power OFF TX	Poff			-30	dBm	
Optical Wavelength	λλ	840	855	860	nm	
Optical Return Loss Tolerance	ORLT			12	dB	
Relative Intensity Noise	RIN			-128	dB/Hz	
Eye Diagram		Compatible with IEEE 802.3-2005				
Receiver						
Receiver Sensitivity	PIN			-9.9	dBm	
Receiver Sensitivity @ FEC rate	PIN-OMA			-11.1	dBm	
Receiver Overload	P_{max}	-1			dBm	
Optical Center Wavelength	λC	840		860	nm	
LOS De-Assert	LOSD			-13	dBm	
LOS Assert	LOSA	-30			dBm	
LOS Hysteresis		0.5			dB	

Pin Descriptions

| Pin	Symbol	Name/Descriptions	Ref.
1	VeeT	Transmitter Ground (Common with Receiver Ground).	1
2	TX Fault	Transmitter Fault. LVTTL-O	2
3	TX Disable	Transmitter Disable. Laser output disabled on high or open. LVTT-I.	3
4	SDA	2-Wire Serial Interface Data Line (Same as MOD-DEF2 in INF-8074i). LVTTL-I/O.	
5	SCL	2-Wire Serial Interface Data Line (Same as MOD-DEF2 in INF-8074i). LVTTL-I.	
6	MOD_ABS	Module Absent, Connect to VeeT or VeeR in Module.	4
7	RS0	Rate Select 0. Not used	5
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation. LVTTL-O.	2
9	RS1	Rate Select 1. Not used	5
10	VeeR	Receiver Ground (Common with Transmitter Ground).	1
11	VeeR	Receiver Ground (Common with Transmitter Ground).	1
12	RD-	Receiver Inverted DATA out. AC Coupled. CML-O.	
13	RD+	Receiver Non-inverted DATA out. AC Coupled. CML-O.	
14	VeeR	Receiver Ground (Common with Transmitter Ground).	1
15	VccR	Receiver Power Supply.	
16	VccT	Transmitter Power Supply.	
17	VeeT	Transmitter Ground (Common with Receiver Ground).	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled. CML-I.	
19	TD-	Transmitter Inverted DATA in. AC Coupled. CML-O.	
20	VeeT	Transmitter Ground (Common with Receiver Ground).	1

Notes:

1. The module signal ground contacts, VeeR and VeeT, should be isolated from the module case.
2. This contact is an open collector/drain output and should be pulled up to the Vcc_Host with resister in the range 4.7KΩ to 10KΩ. Pull ups can be connected to one or several power supplies, however the host board design shall ensure that no module contract has voltage exceeding module VccT/R +0.5.V.
3. Tx_Disable is an input contact with a 4.7KΩ to 10KΩ pull-up resistor to VccT inside module.
4. Mod_ABS is connected to VeeT or VeeR in the SFP+ module. The host may pull the contract up to Vcc_Host with a resistor in the range from 4.7KΩ to 10KΩ. Mod_ABS is asserted "High" when the SFP+ module is physically absent from a host slot.
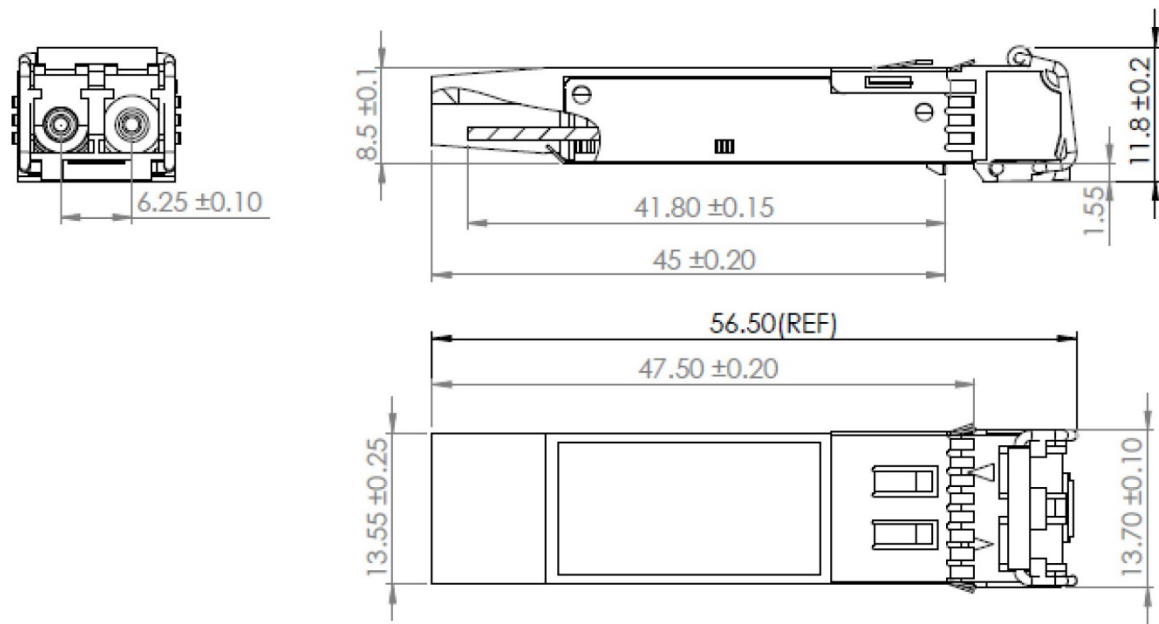5. Internally pulled down per SFF-8431
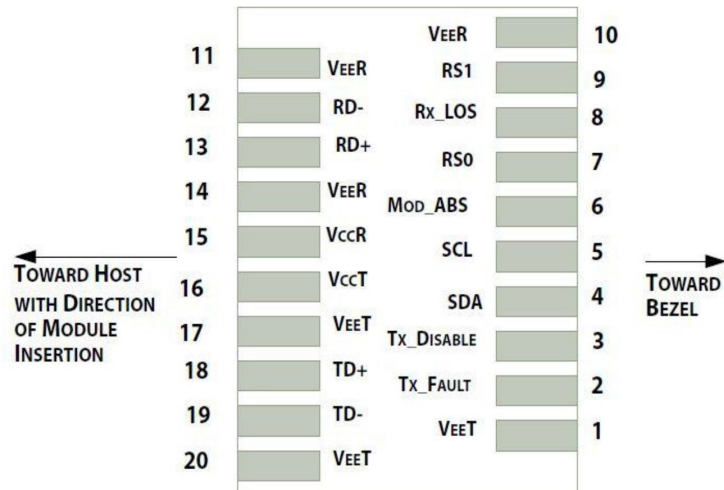
Pin-out of connector Block on Host board

Recommended Circuit Schematic
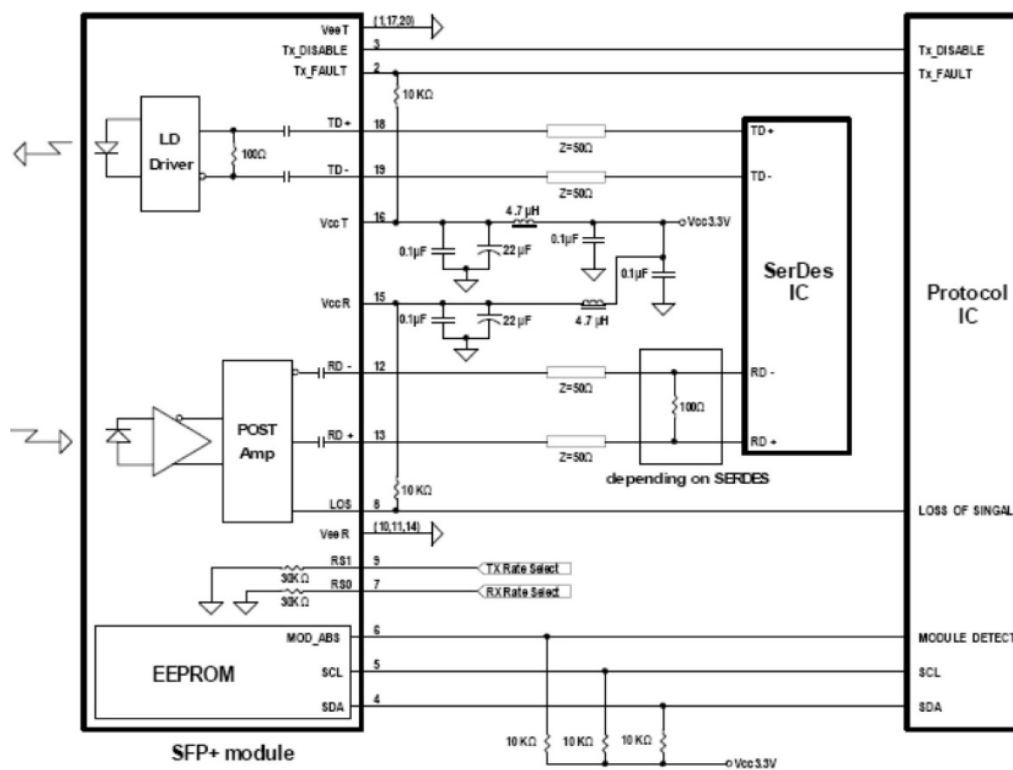
Mechanical Specifications

Small Form Factor Pluggable (SFP) transceivers are compatible with the dimensions defined by the SFP Multi-Sourcing Agreement (MSA).

EEPROM Information

EEPROM memory map specific data field description is as below: