

472580A-OPC

Alcatel-Lucent Nokia® 472580A Compatible TAA 6GBase-LR SFP+ Transceiver (SMF, 1310nm, 15km, LC, DOM)

Features

- SFF-8432 and SFF-8472 Compliance
- Duplex LC Connector
- Single-mode Fiber
- Commercial Temperature 0 to 70 Celsius
- Hot Pluggable
- Metal with Lower EMI
- Excellent ESD Protection
- RoHS Compliant and Lead Free



Applications:

- 6GBase Ethernet
- Access and Enterprise

Product Description

This Alcatel-Lucent Nokia® 472580A compatible SFP+ transceiver provides 6GBase-LR throughput up to 15km over single-mode fiber (SMF) using a wavelength of 1310nm via an LC connector. It is guaranteed to be 100% compatible with the equivalent Alcatel-Lucent Nokia® transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit
Storage Temperature	Tstg	-40		85	°C
Relative Humidity	RH	5		95	%
Supply Voltage	Vcc	-0.5		4.0	V
Operating Temperature	Tc	0	25	70	°C
Data Rate			6.144		Gbps

Electrical Characteristics

Parameter		Symbol	Min.	Typ.	Max.	Unit	Notes
Module Supply Current		Icc			300	mA	
Power Dissipation		P _{DISS}			1000	mW	
Supply Voltage		Vcc	3.135	3.3	3.465	V	
Transmitter							
Input Differential Impedance		ZIN		100		Ω	
Differential Data Input Swing		VIN,pp	180		700	mVp-p	
Tx_Fault	Transmitter Fault	VOH	2.0		Host_Vcc	V	
	Normal Operation	VOL	0		0.8	V	
Tx_Disable	Transmitter Disable	VIH	2.0		Host_Vcc	V	
	Transmitter Enable	VIL	0		0.8	V	
Receiver							
Output Differential Impedance		ZOUT		100		Ω	
Differential Data Output Swing		VOUT,pp	300		850	mVp-p	1
Data Output Rise/Fall Time		Tr/Tf	28			ps	2
Rx_LOS	Loss of Signal (LOS)	VOH	2.0		Host_Vcc	V	3
	Normal Operation	VOL	0		0.8	V	3

Notes:

1. Internally AC coupled but requires an external 100Ω differential load termination.
2. 20-80%.
3. LOS is an open collector output that should be pulled up with 4.7kΩ on the host board.

Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Transmitter						
Launch Optical Power	Po	-8.2		+0.5	dBm	1
Center Wavelength Range	λC	1260	1310	1355	nm	
Extinction Ratio	ER	3.5			dB	2
Optical Modulation Amplitude	OMA	-5.2			dBm	
Spectral Width (-20dB)	Δλ			1	nm	
Side-Mode Suppression Ratio	SMSR	30			dB	
Transmitter and Dispersion Penalty	TDP			3.2	dB	
Optical Return Loss Tolerance	ORLT			12	dB	
POUT @Tx_Disable Asserted	Poff			-30	dBm	1
Eye Diagram	IEEE Std 802.3-2005 10Gb Ethernet 10GBASE-LR Compatible					
Receiver						
Center Wavelength	λC	1260	1310	1355	nm	
Receiver Sensitivity (Pavg)	S			-14.4	dBm	3
Receiver Sensitivity (POMA)	SOMA			-12.6	dBm	3
Receiver Overload (Pavg)	POL	0.5			dBm	3
Stressed Sensitivity (OMA)				-10.3	dBm	
Optical Return Loss	ORL	12			dB	
LOS De-Assert	LOSD			-17	dBm	
LOS Assert	LOSA	-30			dBm	
LOS Hysteresis		0.5			dB	

Notes:

1. The optical power is launched into 9/125μm SMF.
2. Measured with a PRBS 2³¹-1 test pattern @6.144Gbps.
3. Measured with PRBS 2³¹-1 test pattern @6.144Gbps with BER<10⁻¹².

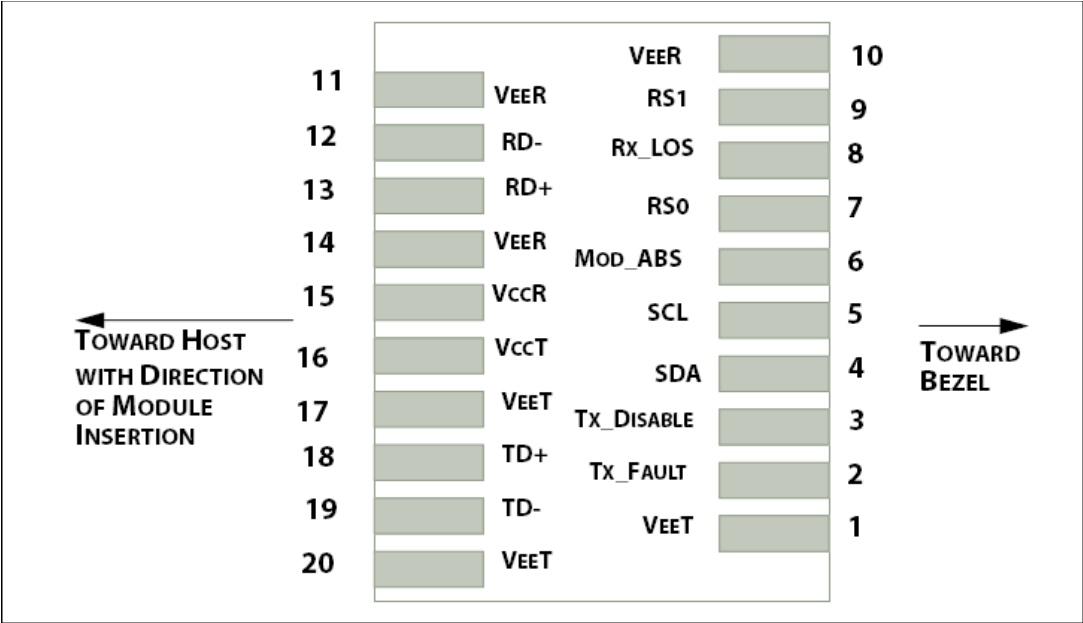
Pin Descriptions

Pin	Symbol	Name/Description	Notes
1	VeeT	Transmitter Ground.	1
2	Tx_Fault	Transmitter Fault (LVTTTL-O). "High" indicates a fault condition.	2
3	Tx_Disable	Transmitter Disable (LVTTTL-I). "High" or "open" disables the transmitter.	3
4	SDA	2-Wire Serial Interface Data (LVCMOS-I/O) (MOD-DEF2).	4
5	SCL	2-Wire Serial Interface Clock (LVCMOS-I/O) (MOD-DEF1).	4
6	MOD_ABS	Module Absent (Output). Connected to the VeeT or VeeR in the module.	5
7	RS0	Rate Select 0. Not used. Presents high input impedance.	
8	Rx_LOS	Receiver Loss of Signal (LVTTTL-O).	2
9	RS1	Rate Select 1. Not Used. Presents high input impedance.	
10	VeeR	Receiver Ground.	1
11	VeeR	Receiver Ground.	1
12	RD-	Inverse Received Data Out (CML-O).	
13	RD+	Received Data Out (CML-O).	
14	VeeR	Receiver Ground.	
15	VccR	+3.3V Receiver Power.	
16	VccT	+3.3V Transmitter Power.	
17	VeeT	Transmitter Ground.	1
18	TD+	Transmitter Data In (CML-I).	
19	TD-	Inverse Transmitter Data In (CML-I).	
20	VeeT	Transmitter Ground.	1

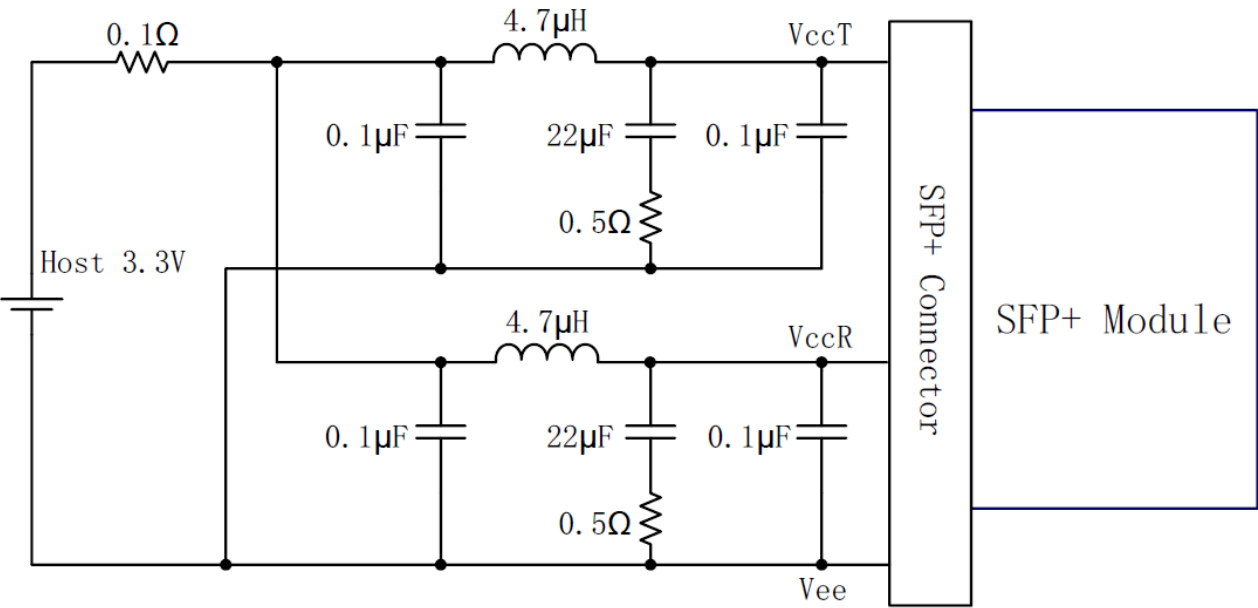
Notes:

1. The module signal grounds are isolated from the module case.
2. This is an open collector/drain output that, on the host board, requires a 4.7k Ω to 10k Ω pull-up resistor to the Host_Vcc.
3. This input is internally biased "high" with a 4.7k Ω to 10k Ω pull-up resistor to the VccT.
4. 2-Wire Serial Interface Clock and Data lines require an external pull-up resistor dependent on the capacitance load.
5. This is a ground return that, on the host board, requires a 4.7k Ω to 10k Ω pull-up resistor to the Vcc_Host.

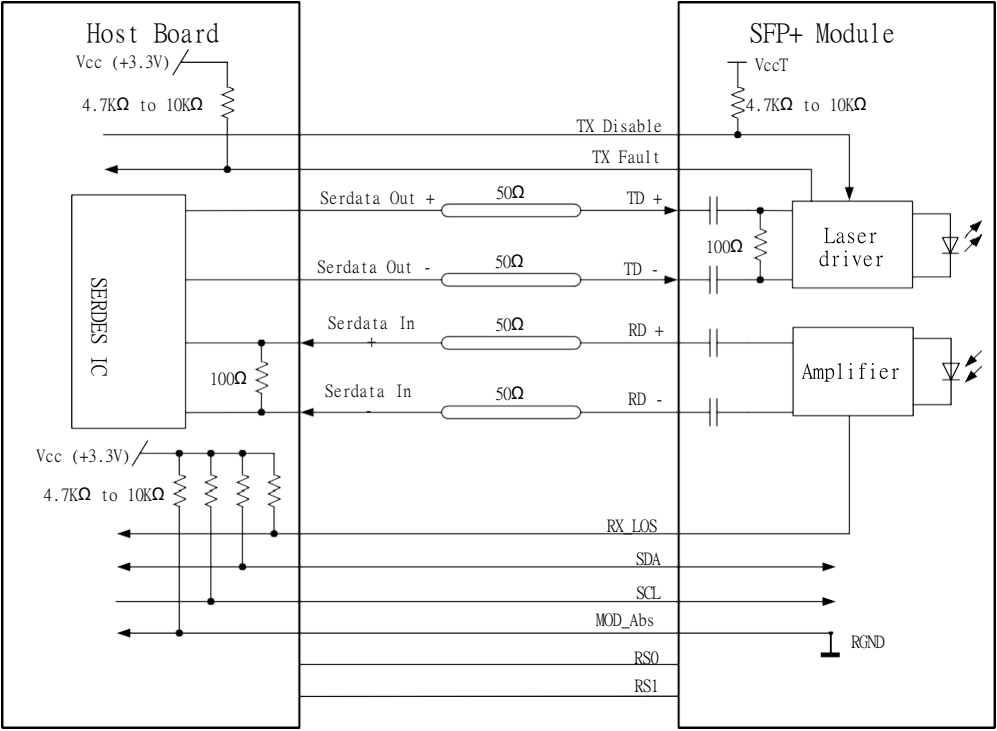
Electrical Pad Layout



Recommended Host Board Power Supply Filter Network



Recommended Application Interface Block Diagram



Mechanical Specifications

