

## GPON-SFP-ONUS-SC-I-AO

MSA and TAA GPON ONU Stick SFP Transceiver (SMF, 1310nmTX/1490nmRX, 1.25G/2.5G, 20km, SC, Stick, -40 to 85C)

### Features

- 2x10 electrical SFP connector
- Single fiber bi-directional with 1244Mbps upstream
- Single fiber bi-directional with 2488Mbps downstream
- Supporting 1000BASE-X/2500BASE-X/SGMII/HSGMII interface
- Single-mode fiber
- SC/UPC optical connector
- Highly flexible 802.1Q VLAN support
- Downstream AES decryption
- RoHS compliant and Lead-Free
- Industrial temperature: -40 to 85 Celsius



### Applications

- Network switch, router and MDU

### Product Description

This Industry Standard SFP transceiver provides 2.5Gbps/1.25Gbps-PON throughput up to 20km over single-mode fiber (SMF) using a wavelength of 1310nmTx/1490nmRx via a SC connector. It is guaranteed to be 100% compatible with the equivalent Industry Standard transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. It is built to meet or exceed the specifications of Industry Standard, as well as to comply with MSA (Multi-Source Agreement) standards to ensure seamless network integration. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

AddOn's transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. – made or designated country end products."



## Absolute Maximum Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Maximum Supply Voltage	V <sub>CC</sub>	GND		3.6	V	1
Storage Ambient Temperature	T <sub>stg</sub>	-40		85	°C	1
Operating Case Temperature	T <sub>c</sub>	-40	25	85	°C	1
Relative Humidity - Storage	RH <sub>stg</sub>	0		95	%	1
Relative Humidity - Operating	RH <sub>op</sub>	5		80	%	1
Optical Upstream Data Rate	BR		1244		Mbps	
Optical Downstream Data Rate	BR		2488		Mbps	
Tx_DIS Logic - High State	Tx_DIS <sub>H</sub>	2.0		V <sub>CC</sub>	V	LVTTTL
Tx_DIS Logic - Low State	Tx_DIS <sub>L</sub>	0		0.8	V	LVTTTL
Tx_Fault Logic - High State	Tx_Fault <sub>H</sub>	2.4			V	2
Tx_Fault Logic - Low State	Tx_Fault <sub>L</sub>			0.4	V	2
Rx_LOS Logic - High State	Rx_LOS <sub>H</sub>	2.4			V	2
Rx_LOS Logic - Low State	Rx_LOS <sub>L</sub>			0.4	V	2

### Notes:

1. Exceeding the Absolute Maximum Ratings may cause irreversible damage to the device. The device is not intended to be operated under the condition of simultaneous Absolute Maximum Ratings, a condition which may cause irreversible damage to the device.
2. LVTTTL (open collector/drain).

## Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Module Supply Voltage	V <sub>CC</sub>	3.135	3.30	3.465	V	
Module Supply Current (Tx and Rx)	I <sub>IN</sub>		550		mA	
Module Power Consumption	PD			2	W	
<b>Transmitter</b>						
Tx_Data Differential Input Voltage	V <sub>ID</sub>	300		1200	mV	SGMII Mode
Bit Rate (Tx)	BR <sub>Tx</sub>		1250		Mbps	SGMII Mode
Tx_DIS - High (Transmitter Off)	VO <sub>H</sub>	2.0		V <sub>CC</sub>	V	
Tx_DIS - Low (Transmitter On)	VO <sub>L</sub>	0		0.8	V	
<b>Receiver</b>						
Rx_Data Differential Output Voltage	V <sub>OD</sub>	300		800	mV	1250Mbps
Rx_Data Differential Output Voltage	V <sub>OD</sub>	500		1000	mV	2500Mbps
Rx_LOS Logic – High State	V <sub>LOS<sub>H</sub></sub>	2.4			V	1
Rx_LOS Logic – Low State	V <sub>LOS<sub>L</sub></sub>			0.8	V	1

### Notes:

1. LVTTTL (open collector/drain).

## Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
<b>Transmitter</b>						
Transmitter Type		1310nm DFB Burst Mode				
Upstream Signaling Speed	Sup		1244		Mbps	
Average Output Power (9/125μm SMF)	POUT	0		5.0	dBm	1
Optical Output with Tx Off	POUT			-40	dBm	
Tx Wavelength	$\lambda$	1290	1310	1330	nm	
Spectral Line Width @-20dB	$\Delta\lambda$			1.0	nm	
Extinction Ratio	ER	10			dB	2
Side-Mode Suppression Ratio	SMSR	30			dB	
<b>Receiver</b>						
Receiver Type		1490nm CW Mode				
Downstream Signaling Speed	Sdown		2488		Mbps	
Optical Center Wavelength	$\lambda$	1480	1490	1500	nm	
Receiver Sensitivity	PIN			-28.0	dBm	3
Receiver Optical Overload	PIN(SAT)	-8			dBm	3
Rx_LOS of Signal Asserted	P <sub>A</sub>	-45			dBm	
Rx_LOS of Signal De-Asserted	P <sub>D</sub>			-28.5	dBm	
Rx_LOS of Signal Hysteresis	P <sub>H</sub>	0.5		6.0	dB	

### Notes:

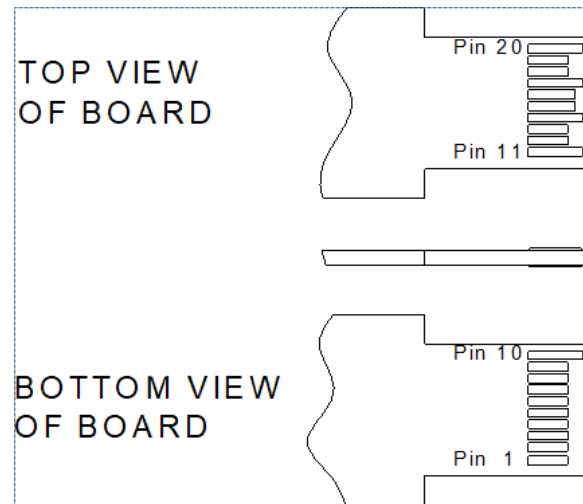
1. Measured with 9/125μm G.652 SMF.
2. Measured by Ethernet package with random payload.
3. BER<10<sup>-10</sup>, @2488Mbps, PRBS 2<sup>23</sup>- 1, and ER=11dB.

## Pin Descriptions

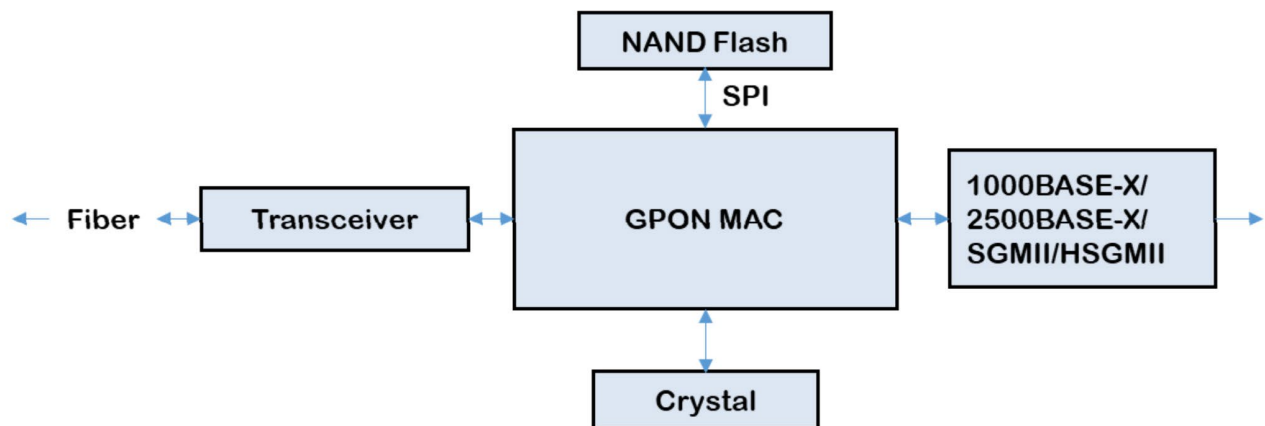
Pin	Symbol	Name/Description	Notes
1	VeeT	Transmitter Ground.	
2	Tx_Fault	Transmitter Fault. Low = Normal Operation. High = Fault Indication.	1
3	Tx_Disable	Transmitter Disable. Low = Normal Operation. High = Disables Module.	1
4	MOD_DEF2	Module Definition 2. 2-Wire Serial Interface Data.	1
5	MOD_DEF1	Module Definition 1. 2-Wire Serial Interface Clock.	1
6	MOD_DEF0	Module Definition 0. Presence Pin. The MOD_DEF0 signal is set to low level after initialization of $\mu$ C and power up the I <sup>2</sup> C interface.	
7	Dying Gasp	Dying Gasp Indication. When “high,” indicates normal operation. “Low” indicates power failure.	4
8	LOS	Loss of Signal. When “high” indicates no optical power. “Low” indicates normal operation.	1
9	PIN9	Reserved. Keep floating when not used.	
10	VeeR	Receiver Ground.	
11	VeeR	Receiver Ground.	
12	RD-	Rx_Data Output Inverted.	2
13	RD+	Rx_Data Output Non-Inverted.	2
14	VeeR	Receiver Ground.	
15	VccR	Receiver DC Power.	3.3V±5%
16	VccT	Transmitter DC Power.	3.3V±5%
17	VeeT	Transmitter Ground.	
18	TD+	Tx_Data Input Non-Inverted.	3
19	TD-	Tx_Data Input Inverted.	3
20	VeeT	Transmitter Ground.	

### Notes:

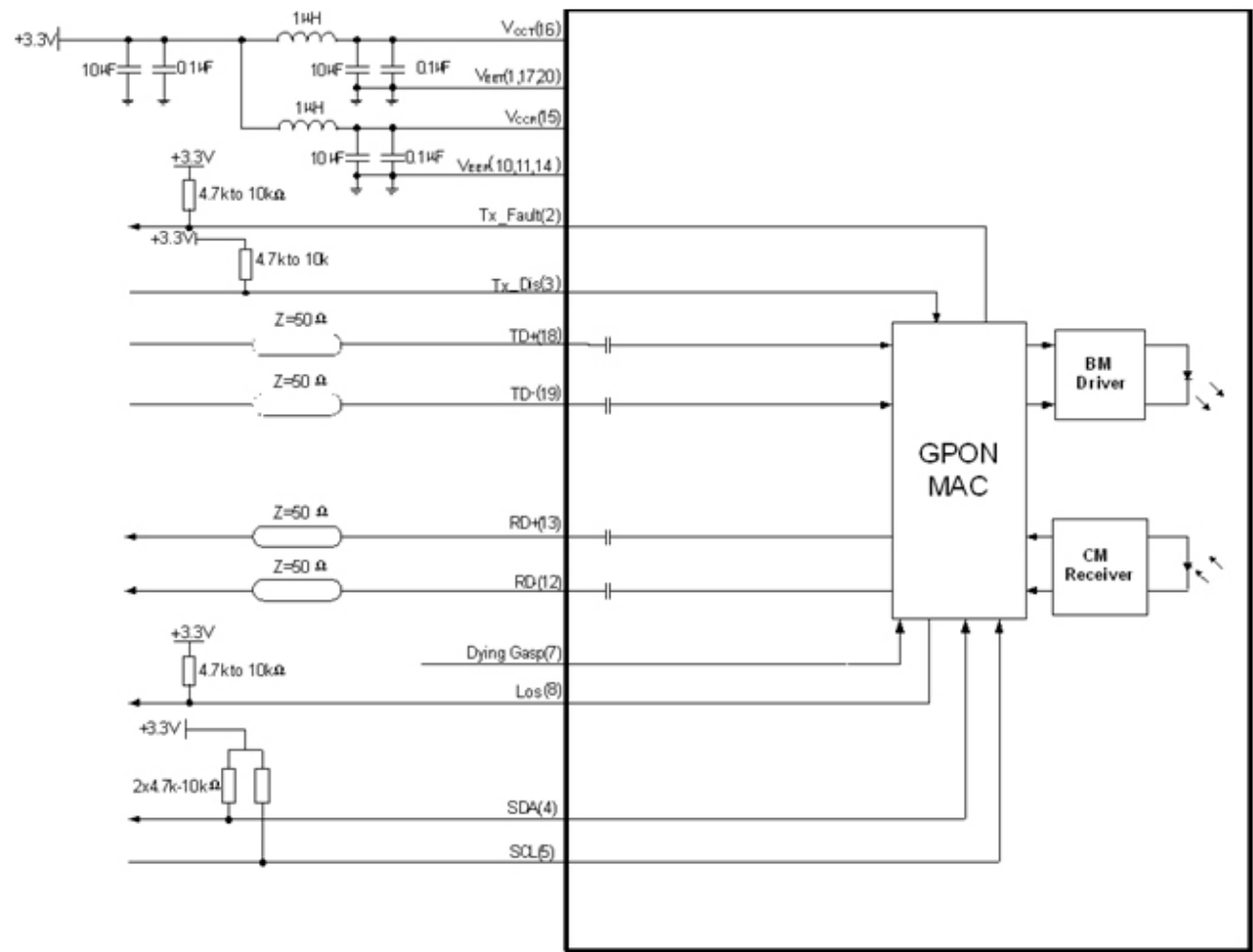
1. The uncommitted Tx\_Fault, Tx\_Disable, MOD\_DEF2 ,MOD\_DEF1, and LOS monitor and control pins each require a pull up resistor of 4.7k $\Omega$  to 10k $\Omega$ . The pull-up voltage must be 3.3V.
2. The 100 $\Omega$  differential Rx Data Output is internally AC coupled. Supporting 1000BASE-X/2500BASE-X/SGMII/HSGMII interface.
3. The 100 $\Omega$  differential Tx Data Input is internally AC coupled. Supporting 1000BASE-X/2500BASE-X/SGMII/HSGMII interface.
4. Voltage Detect Input for Dying Gasp. When the voltage on this pin is lower than 1.29V+/-5%, a dying gasp event is triggered. A 4.7k $\Omega$  resistor is used to pull up to DC power in the module.



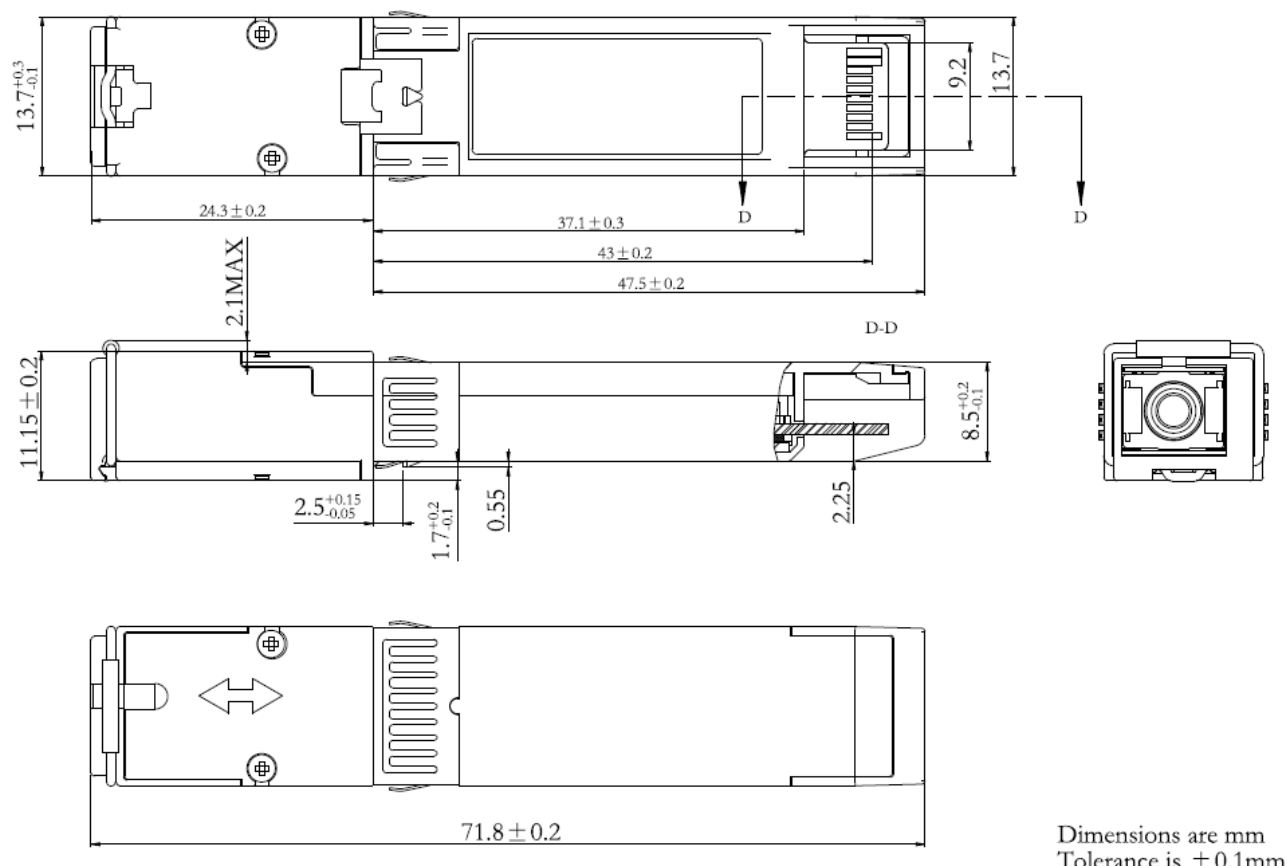
### Block Diagram



Electrical Interface



Mechanical Specifications



## About AddOn Networks

In 1999, AddOn Networks entered the market with a single product. Our founders fulfilled a severe shortage for compatible, cost-effective optical transceivers that compete at the same performance levels as leading OEM manufacturers. Adhering to the idea of redefining service and product quality not previously had in the fiber optic networking industry, AddOn invested resources in solution design, production, fulfillment, and global support.

Combining one of the most extensive and stringent testing processes in the industry, an exceptional free tech support center, and a consistent roll-out of innovative technologies, AddOn has continually set industry standards of quality and reliability throughout its history.

Reliability is the cornerstone of any optical fiber network and is ingrained in AddOn's DNA. It has played a key role in nurturing the long-term relationships developed over the years with customers. AddOn remains committed to exceeding industry standards with certifications from ranging from NEBS Level 3 to ISO 9001:2005 with every new development while maintaining the signature reliability of its products.



## U.S. Headquarters

Email: [sales@addonnetworks.com](mailto:sales@addonnetworks.com)

Telephone: +1 877.292.1701

Fax: 949.266.9273

## Europe Headquarters

Email: [salesupportemea@addonnetworks.com](mailto:salesupportemea@addonnetworks.com)

Telephone: +44 1285 842070