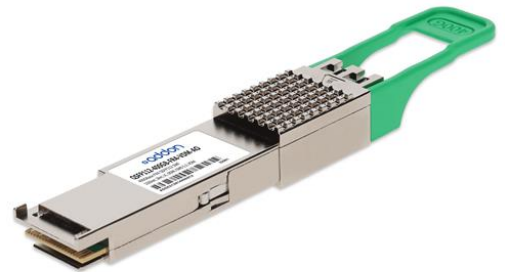


QSFP112-400GB-FR4-VDM-AO

MSA Compliant 400GBase-FR4 QSFP112 Transceiver (SMF, 1310nm, 2km, LC, DOM, CMIS 5.2, VDM)

Features:

- 4 Independent Electrical Input/Output Channels
- Hot-Pluggable
- LC Optical Connector
- Cooled CWDM4 EML/PIN PD Array
- QSFP112 MSA Compatible Electrical I/O
- 2km Link Over SMF
- CMIS 5.2 Compliant
- +3.3V Power Supply
- Operating Temperature: 0 to 70 Celsius
- IEEE 802.3cu CL 151 Compliant
- RoHS Compliant and Lead-Free



Applications:

- HPC Interconnect, Data Centers, & Cloud Networks
- 400GBase Ethernet
- CMIS Versatile Diagnostic Monitoring (VDM) Enabled

Product Description

This MSA compliant QSFP112 transceiver provides 400GBase-FR4 throughput up to 2km over single-mode fiber (SMF) using a wavelength of 1310nm via an LC connector with VDM. It can operate at temperatures between 0 and 70C. All of our transceivers are built to comply with Multi-Source Agreement (MSA) standards and are uniquely serialized and tested for data-traffic and application to ensure seamless network integration. Additional product features include Digital Optical Monitoring (DOM) support which allows access to real-time operating parameters. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

AddOn's transceivers are RoHS compliant and lead-free.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit	Notes
Storage Ambient Temperature	Tstg	-40	+85	°C	
Relative Humidity – Storage	RHs	15	85	%	1
Relative Humidity – Operating	RHo	15	85	%	1
Module Supply Voltage	Vcc	-0.5	3.6	V	

Notes:

1. RH is a non-condensing condition.
2. Exceeding the Absolute Maximum Ratings may cause irreversible damage to the device. The device is not intended to be operated under the condition of simultaneous Absolute Maximum Ratings, a condition which may cause irreversible damage to the device.

Recommended Operating Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
PAM4 Signaling Rate Per Lane	Rate		53.125		GBaud	1
Operating Case Temperature	Tc	0	+25	70	°C	2
Module Supply Voltage	Vcc	3.135	3.3	3.465	V	
Power Consumption	PC			10	W	

Notes:

1. Range: $\pm 100\text{ppm}$.
2. Temperature range = C.

Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Transmitter						
Signaling Rate Per Lane		53.125 $\pm$ 100ppm			GBaud	
Tx_Data Differential Input Voltage	VIN			900	mV	
Tx_Data Differential Input Impedance	ZIN	90	100	110	Ω	
Receiver						
Signaling Rate Per Lane		53.125 $\pm$ 100ppm			GBaud	
Rx_Data Differential Output Voltage	VOOUT			900	mV	
Rx_Data Differential Output Impedance	ZOUT	90	100	110	Ω	

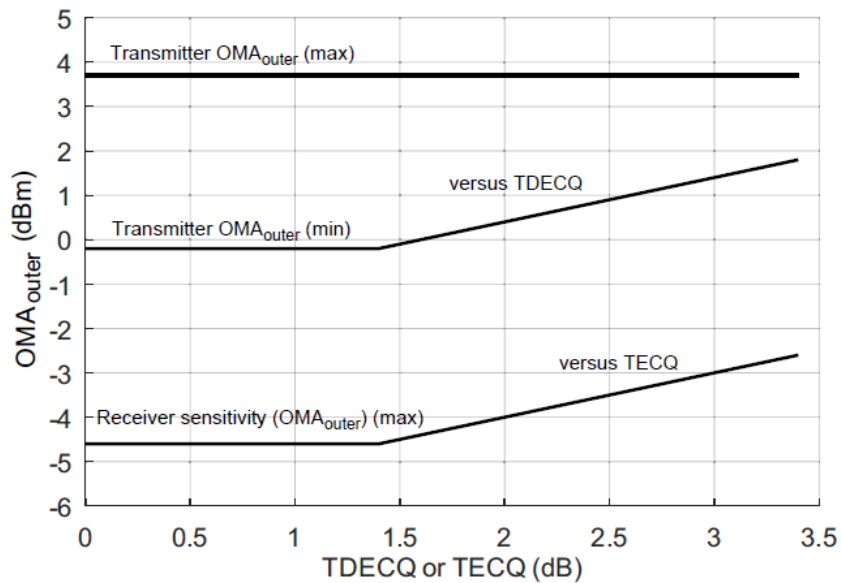
Optical Characteristics

Parameter		Symbol	Min.	Typ.	Max.	Unit	Notes
Transmitter							
PAM4 Signaling Rate Per Lane				53.125		GBaud	1
Transmitter Type			CWDM EML				
Average Total Optical Power		PTotal			10.4	dBm	
Average Launch Power Per Lane		POUT	-3.2		4.4	dBm	2
Optical Output w/ Tx Off Per Lane		Poff			-16	dBm	
Center Wavelength Each Lane		λ	1264.5		1277.5	nm	
			1284.5		1297.5		
			1304.5		1317.5		
			1324.5		1337.5		
Extinction Ratio Per Lane (Minimum)		ER	3.5			dB	
OMAAouter Per Lane (Maximum)		OMA			3.7	dBm	
Outer Optical Modulation Amplitude (OMAAouter) Per Lane Minimum	For TDECQ < 1.4dB	OMA	-0.2			dBm	
	For 1.4dB ≤ TDECQ < 3.4dB		TDECQ-1.6				
Transmitter Power Excursion (Maximum)					1.8	dBm	
Optical Return Loss Tolerance (Maximum)		ORL			17.1	dB	
Side-Mode Suppression Ratio (Minimum)		SMSR	30			dB	
Transmitter and Dispersion Eye Closure for PAM4 Per Lane (Maximum)		TDECQ			3.4	dB	
RIN _{17.1} OMA					-136	dB/Hz	
Transmitter Reflectance (Maximum)		TR			-26	dB	
Receiver							
Receiver Type			CWDM and PIN/TIA				
Damage Threshold Per Lane		THd	5.4			dBm	
Receiver Sensitivity OMAouter Per Lane	For TECQ < 1.4dB	PIN			-4.6	dBm	3
	For 1.4dB ≤ TECQ ≤ 3.4dB				-6+TECQ		
Receive Power Per Lane (OMAAouter) (Maximum)		PINouter			3.7	dBm	
Average Receive Power Per Lane		P	-7.2		4.4	dBm	4
Receiver Reflectance		RR			-26	dB	
Difference in Receive Power Between Any Two Lanes (OMAAouter) (Maximum)		Pdif			4.1	dB	
Center Wavelength		λ	1264.5		1277.5	nm	5
			1284.5		1297.5		
			1304.5		1317.5		
			1324.5		1337.5		

Rx_LOS of Signal - Assert	LOSA	-20			dBm	
Rx_LOS of Signal - De-Assert	LOSD			-9	dBm	

Notes:

1. Range: $\pm 100\text{ppm}$.
2. Average optical output.
3. Pre-FEC BER $< 1\text{E}^{-6}$. The values for receiver sensitivity ($\text{OMA}_{\text{outer}}$), per lane (maximum), vary depending on the value of TECQ for the test source used for measurement.
4. Average receive power, per lane (minimum), is informative and not the principal indicator of signal strength. A receive power below this value can't be compliant; however, a value above this does not ensure compliance.
5. For each optical port.
6. Transmitter $\text{OMA}_{\text{outer}}$ per lane versus TDECQ and receiver sensitivity ($\text{OMA}_{\text{outer}}$) per lane versus TECQ for 400GBASE-FR4.



Pin Descriptions

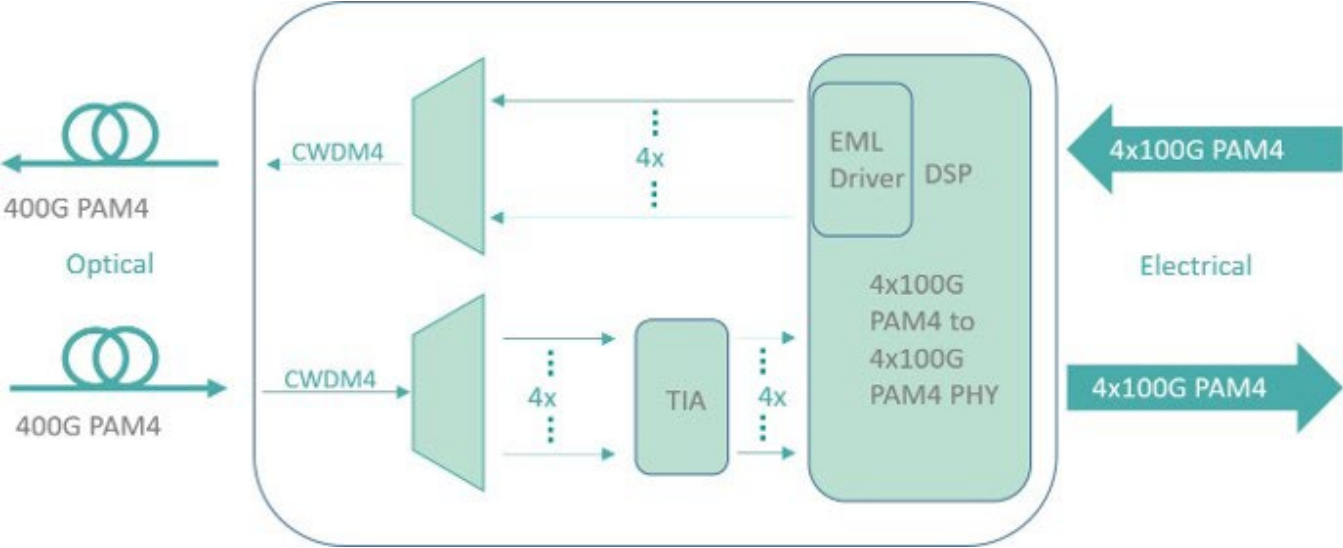
Pin	Symbol	Name/Description	Notes
1	GND	Module Ground.	
2	TX2n	Transmitter Inverted Data Input.	
3	TX2p	Transmitter Non-Inverted Data Input.	
4	GND	Module Ground.	
5	TX4n	Transmitter Inverted Data Input.	
6	TX4p	Transmitter Non-Inverted Data Input.	
7	GND	Module Ground.	
8	ModSelL	Module Select.	
9	ResetL	Reset.	
10	VccRx	+3.3 V Power supply receiver.	
11	SCL	2-wire serial interface clock.	
12	SDA	2-wire serial interface data.	
13	GND	Module Ground.	
14	RX3p	Receiver Non-Inverted Data Output.	
15	RX3n	Receiver Inverted Data Output.	
16	GND	Module Ground.	
17	RX1p	Receiver Non-Inverted Data Output.	
18	RX1n	Receiver Inverted Data Output.	
19	GND	Module Ground.	
20	GND	Module Ground.	
21	RX2n	Receiver Inverted Data Output.	
22	RX2p	Receiver Non-Inverted Data Output.	
23	GND	Module Ground.	
24	RX4n	Receiver Inverted Data Output.	
25	RX4p	Receiver Non-Inverted Data Output.	
26	GND	Module Ground.	
27	ModPrsL	Present.	
28	IntL/RxLOS	Interrupt/optional RxLOS.	
29	VccTx	+3.3 V Power supply transmitter.	
30	Vcc1	+3.3 V Power Supply.	
31	LPMode/TxDis	Low Power Mode/optional TX Disable.	
32	GND	Module Ground.	
33	TX3p	Transmitter Non-Inverted Data Input.	
34	TX3n	Transmitter Inverted Data Input.	
35	GND	Module Ground.	

36	TX1p	Transmitter Non-Inverted Data Input.	
37	TX1n	Transmitter Inverted Data Input.	
38	GND	Module Ground.	

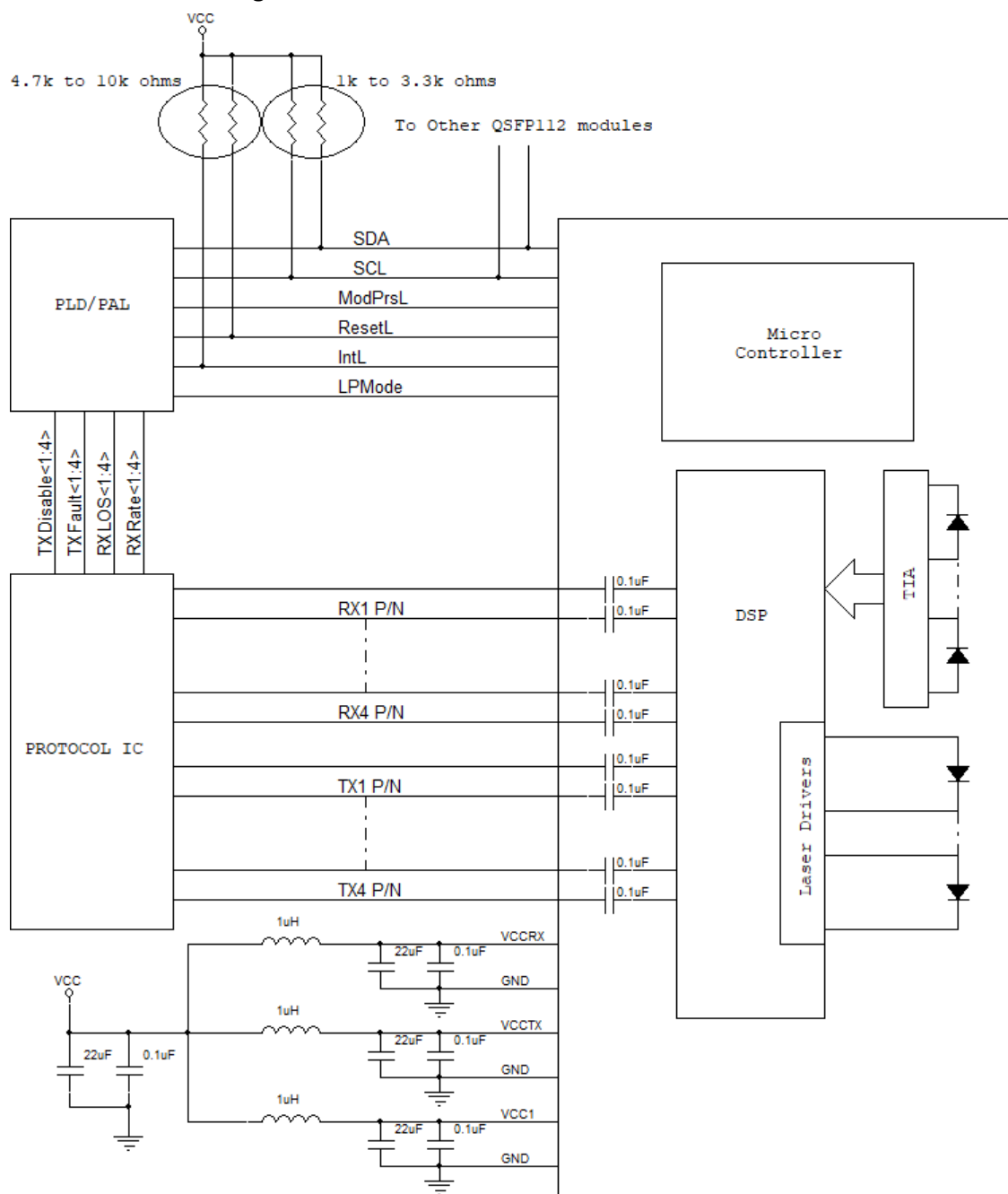
Pin Assignments



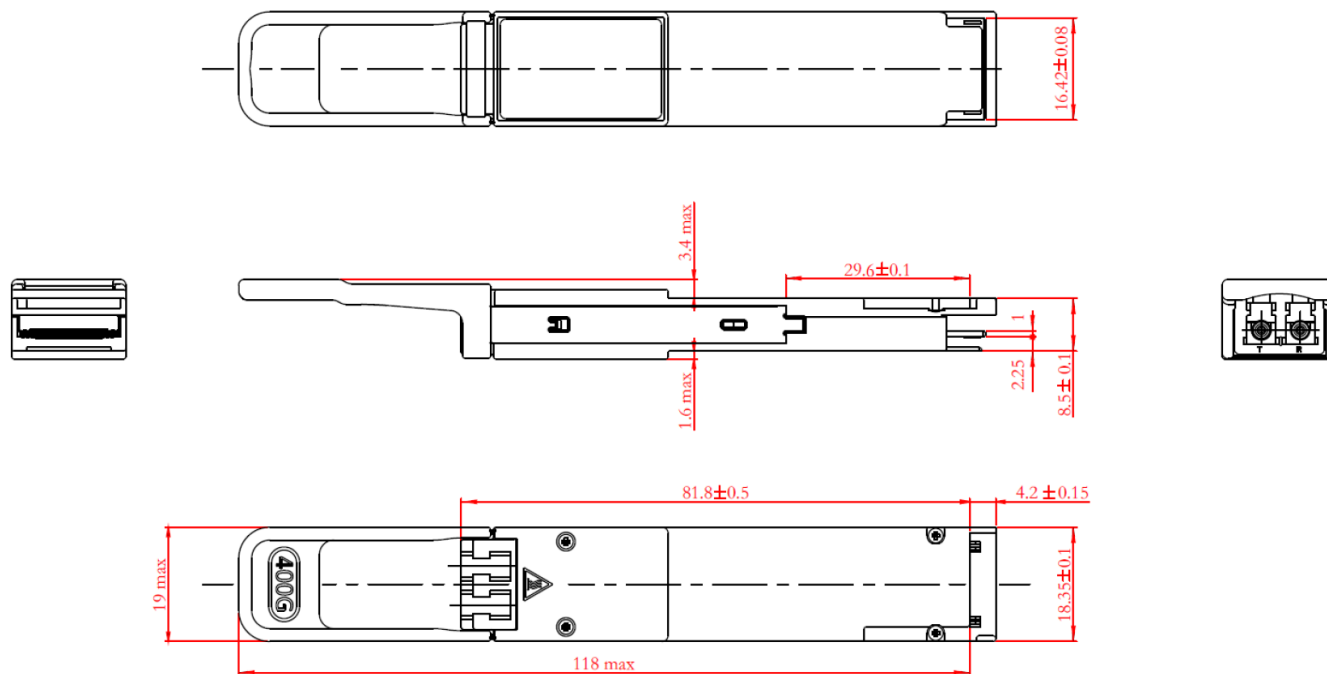
Transceiver Block Diagram



Host Board and Module Board Diagram



Mechanical Specifications



Notes:

1. Tolerance: ± 0.1 mm.
2. Others according with QSFP112 MSA or customer specifications.
3. Optical port according to fiber connector specifications.

About AddOn Networks

In 1999, AddOn Networks entered the market with a single product. Our founders fulfilled a severe shortage for compatible, cost-effective optical transceivers that compete at the same performance levels as leading OEM manufacturers. Adhering to the idea of redefining service and product quality not previously had in the fiber optic networking industry, AddOn invested resources in solution design, production, fulfillment, and global support.

Combining one of the most extensive and stringent testing processes in the industry, an exceptional free tech support center, and a consistent roll-out of innovative technologies, AddOn has continually set industry standards of quality and reliability throughout its history.

Reliability is the cornerstone of any optical fiber network and is ingrained in AddOn's DNA. It has played a key role in nurturing the long-term relationships developed over the years with customers. AddOn remains committed to exceeding industry standards with certifications ranging from NEBS Level 3 to ISO 9001:2015 with every new development while maintaining the signature reliability of its products.



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