

SFP-10GBASE-ZR-OPC

MSA and TAA 10GBase-ZR SFP+ Transceiver (SMF, 1550nm, 80km, LC, DOM)

Features

- SFF-8432 and SFF-8472 Compliance
- Duplex LC Connector
- Temperature-stabilized EML transmitter and APD receiver
- Single-mode Fiber
- Commercial Temperature 0 to 70 Celsius
- Hot Pluggable
- Metal with Lower EMI
- Excellent ESD Protection
- RoHS Compliant and Lead Free



Applications:

- 10GBase-ZR Ethernet
- 8x/10x Fibre Channel
- Access, Metro and Enterprise

Product Description

This MSA compliant SFP+ transceiver provides 10GBase-ZR throughput up to 80km over single-mode fiber (SMF) using a wavelength of 1550nm via an LC connector. It can operate at temperatures between 0 and 70C. All of our transceivers are built to comply with Multi-Source Agreement (MSA) standards and are uniquely serialized and tested for data-traffic and application to ensure seamless network integration. Additional product features include Digital Optical Monitoring (DOM) support which allows access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Maximum Supply Voltage	V _{CC}	-0.5	4.0	V
Storage Temperature	TS	-40	85	°C
Operating Case Temperature	T _c	0	70	°C
Operating Humidity	RH	5	85	%
Receiver Power	R _{MAX}		-7	dBm
Maximum Bitrate	B _{max}		11.3	Gbps

Electrical Characteristics (TOP=25°C, V_{CC}=3.3Volts)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Power Supply Voltage	V _{CC}	3.15	3.30	3.43	V	
Power Supply Current	I _{CC}			303	mA	
Power Consumption	P _{DISS}			1	W	
Transmitter						
Differential data input swing	V _{in,pp}	120		850	mV	
Input differential impedance	Z _{in}	80	100	120	Ω	
Receiver						
Differential data output swing	V _{out, pp}	300		850	mV	
Output differential impedance	Z _{in}	80	100	120	Ω	

Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Transmitter						
Optical Power (average)	P _{AVE}	0		4	dBm	1
Transmitter and Dispersion Penalty	TDP			3	dB	
Optical Extinction Ratio	ER	9			dB	
Optical Wavelength	Tλ	1530	1550	1565	nm	
Insertion loss	IL		0.7			
Receiver						
Receiver Sensitivity (average)	R _{AVE}			-24	dBm	2
Receiver overload	P _{max}	-7			dBm	3
Receiver wavelength	Rλ	1260		1565	nm	

Notes:

1. Coupled into a Single-mode fibre
2. Average power, back-to-back, @10.31Gbps, BER 1E-12, PRBS 231-1. TDP not included.
3. Exceeding the Receiver overload can physically damage the module. Please use appropriate attenuation.

Pin Descriptions

Pin	Symbol	Name/Descriptions	Ref.
1	VeeT	Transmitter Ground (Common with Receiver Ground).	1
2	TX Fault	Transmitter Fault. LVTTTL-O	2
3	TX Disable	Transmitter Disable. Laser output disabled on high or open. LVTTTL-I.	3
4	SDA	2-Wire Serial Interface Data Line (Same as MOD-DEF2 in INF-8074i). LVTTTL-I/O.	
5	SCL	2-Wire Serial Interface Data Line (Same as MOD-DEF2 in INF-8074i). LVTTTL-I.	
6	MOD_ABS	Module Absent, Connect to VeeT or VeeR in Module.	4
7	RS0	Rate Select 0. Not used	5
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation. LVTTTL-O.	2
9	RS1	Rate Select 1. Not used	5
10	VeeR	Receiver Ground (Common with Transmitter Ground).	1
11	VeeR	Receiver Ground (Common with Transmitter Ground).	1
12	RD-	Receiver Inverted DATA out. AC Coupled. CML-O.	
13	RD+	Receiver Non-inverted DATA out. AC Coupled. CML-O.	
14	VeeR	Receiver Ground (Common with Transmitter Ground).	1
15	VccR	Receiver Power Supply.	
16	VccT	Transmitter Power Supply.	
17	VeeT	Transmitter Ground (Common with Receiver Ground).	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled. CML-I.	
19	TD-	Transmitter Inverted DATA in. AC Coupled. CML-O.	
20	VeeT	Transmitter Ground (Common with Receiver Ground).	1

Notes:

1. The module signal ground contacts, VeeR and VeeT, should be isolated from the module case.
2. This contact is an open collector/drain output and should be pulled up to the Vcc_Host with resistor in the range 4.7KΩ to 10KΩ. Pull ups can be connected to one or several power supplies, however the host board design shall ensure that no module contract has voltage exceeding module VccT/R +0.5.V.
3. Tx_Disable is an input contact with a 4.7KΩ to 10KΩ pull-up resistor to VccT inside module.
4. Mod_ABS is connected to VeeT or VeeR in the SFP+ module. The host may pull the contract up to Vcc_Host with a resistor in the range from 4.7KΩ to 10KΩ. Mod_ABS is asserted “High” when

5. Internally pulled down per SFF-8431



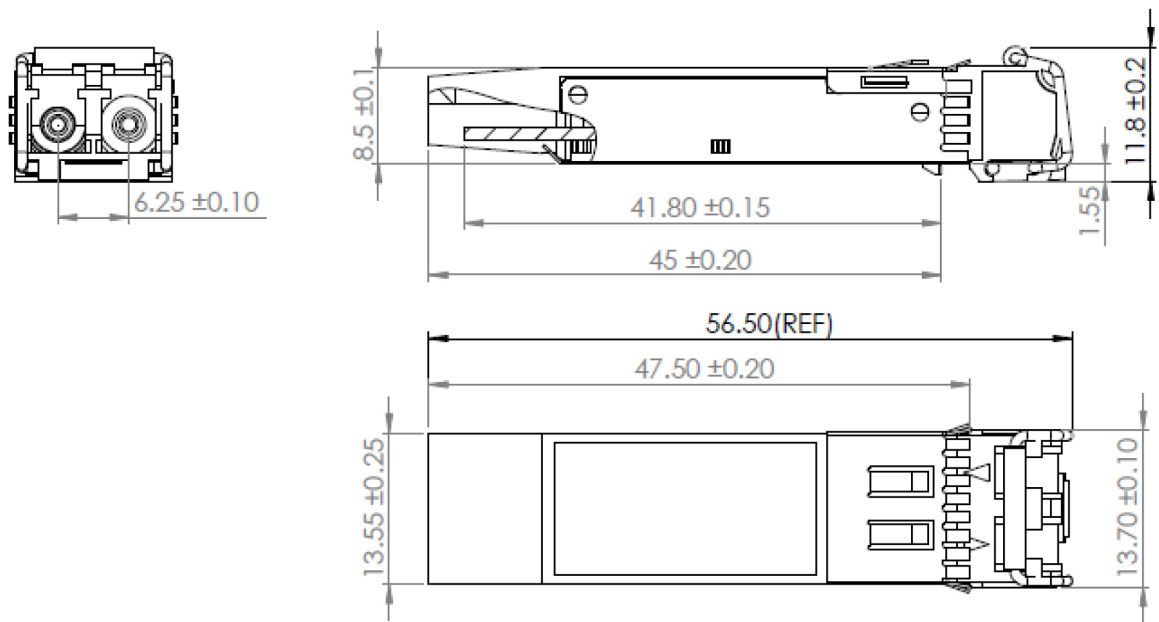
The schematic diagram illustrates the internal architecture of the SerDes IC. Key components and their connections are as follows:

- LD Driver:** Connected to the Tx pin (pin 18) and Tx pin (pin 19). It includes a 100Ω resistor and a 10KΩ pull-up resistor to Vcc T (pin 16).
- POST Amp:** Connected to the Rx pin (pin 12) and Rx pin (pin 13). It includes a 10KΩ pull-up resistor to Vcc R (pin 15).
- EEPROM:** Connected to the MOD_DETECT pin (pin 6), SCL pin (pin 5), and SDA pin (pin 4). It includes a 10KΩ pull-up resistor to Vcc R (pin 15).
- TX Rate Select:** Connected to the TX Rate Select pin (pin 9) and RX Rate Select pin (pin 7).
- RX Rate Select:** Connected to the RX Rate Select pin (pin 7) and TX Rate Select pin (pin 9).
- MOD_DETECT:** Connected to the MOD_DETECT pin (pin 6).
- SCL:** Connected to the SCL pin (pin 5).
- SDA:** Connected to the SDA pin (pin 4).

The diagram also shows the internal circuitry of the SerDes IC, including the LD Driver, POST Amp, and EEPROM, and their connections to the external pins.

Mechanical Specifications

Small Form Factor Pluggable (SFP) transceivers are compatible with the dimensions defined by the SFP Multi-Sourcing Agreement (MSA).



EEPROM Information

EEPROM memory map specific data field description is as below:

2 wire address 1010000X (A0h)		2 wire address 1010001X (A2h)	
0	Serial ID Defined by SFP MSA (96 bytes)	0	Alarm and Warning Thresholds (56 bytes)
95		55	Cal Constants (40 bytes)
127	Vendor Specific (32 bytes)	95	Real Time Diagnostic Interface (24 bytes)
	Reserved, SFF8079 (128 bytes)	119	Vendor Specific (8 bytes)
		127	User Writable EEPROM (120 bytes)
255		247	
		255	Vendor Specific (8 bytes)