SFP-10GB-CW-49-100-OPC

MSA and TAA 10GBase-CWDM SFP+ Transceiver (SMF, 1490nm, 100km, LC, DOM)

Features

- SFF-8432 and SFF-8472 Compliance
- Duplex LC Connector
- Single-mode Fiber
- Commercial Temperature 0 to 70 Celsius
- Hot Pluggable
- Metal with Lower EMI
- Excellent ESD Protection
- RoHS Compliant and Lead Free

Applications:

- 10x Gigabit Ethernet over CWDM
- 8x/10x Fibre Channel
- Access, Metro and Enterprise
- Mobile Fronthaul CPRI/OBSAI

Product Description

This MSA compliant SFP+ transceiver provides 10GBase-CWDM throughput up to 100km over single-mode fiber (SMF) using a wavelength of 1490nm via an LC connector. The listed reach has been determined using a link budget calculation and tested in a standard environment. Actual link distances achieved will be dependent upon the deployed environment. All of our transceivers are built to comply with Multi-Source Agreement (MSA) standards and are uniquely serialized and tested for data-traffic and application to ensure seamless network integration. Additional product features include Digital Optical Monitoring (DOM) support which allows access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

CWDM Available Wavelengths

Wavelengths	Min.	Typ.	Max.
47	1465	1471	1477
49	1485	1491	1497
51	1505	1511	1517
53	1525	1531	1537
55	1545	1551	1557

Absolute Maximum Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit
Maximum Supply Voltage	Vcc	-0.5		4.0	V
Storage Temperature	Tstg	-40		85	°C
Operating Case Temperature	Tc	0	25	70	°C
Bit Error Rate	BER			10^{-12}	
Data Rate	DR	1.2	10.3125	11.3	Gbps

Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Power Supply Voltage	Vcc	3.14	3.30	3.46	V	
Power Supply Current	Icc			550	mA	1
Power Consumption	PC			1.5	W	
Transmitter						
Input Differential Impedance	RIN		100		Ω	
Differential Data Input Swing	VIN,pp	120		1200	mV	
Transmit Disable Voltage	VD	2		Vcc	V	
Transmit Enable Voltage	VEN	Vee		Vee+0.8	V	
Receiver						
Differential Data Output Swing	VOUT,pp	640		1000	mV	
Data Output Rise/Fall Time (20-80%)	Tr/Tf	28			ps	
LOS Fault	VLOSA	2		Host_Vcc	V	
LOS Normal	VLOSD	Vee		Vee+0.5	V	

Notes:
1. For the electrical power interface.

Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Transmitter						
Output Optical Power	P_{TX}	1.5		4	dBm	1
Optical Center Wavelength	λC	1485	1491	1497	nm	
Extinction Ratio	ER	9			dB	
Spectral Width (-20dB)	$\Delta\lambda$			0.6	nm	
Side-Mode Suppression Ratio	SMSR	30			dB	
Relative Intensity Noise	RIN			-128	dB/Hz	
Transmitter Dispersion Penalty	TDP			4	dB	
Transmitter Jitter						2
Launch Power of Off Transmitter	Poff			-30	dBm	1
Receiver						
Optical Center Wavelength	λC	1260		1620	nm	
Average Receive Power	P_{RX}	-25		-7	dBm	
Receiver Sensitivity @10.3Gbps	R_{X_SEN}			-25	dBm	1
Receiver Reflectance	TR_{RX}			-27	dB	
LOS Assert	LOSA	-35			dBm	
LOS De-Assert	LOSD			-27	dBm	
LOS Hysteresis	LOSH	0.5			dB	

Pin Descriptions

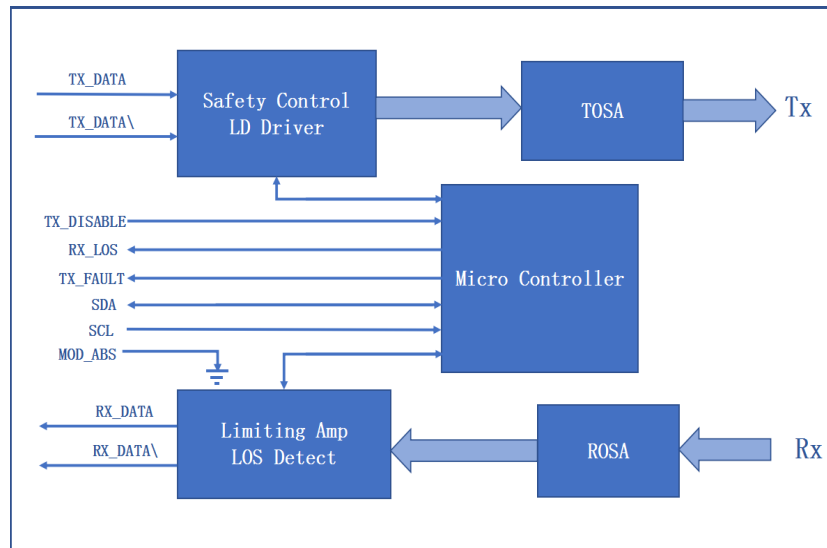| Pin	Symbol	Name/Description	Notes
1	VeeT	Transmitter Ground (Common with Receiver Ground).	1
2	Tx_Fault	Transmitter Fault.	
3	Tx_Disable	Transmitter Disable. Laser output disabled on "high" or "open."	2
4	SDA	2-Wire Serial Interface Data.	3
5	SCL	2-Wire Serial Interface Clock.	3
6	MOD_ABS	Module Absent. Grounded within the module.	3
7	RS0	No Connection Required.	
8	LOS	Loss of Signal Indication. "Logic 0" indicates normal operation.	4
9	RS1	No Connection Required.	1
10	VeeR	Receiver Ground (Common with Transmitter Ground).	1
11	VeeR	Receiver Ground (Common with Transmitter Ground).	1
12	RD-	Receiver Inverted Data Out. AC Coupled.	
13	RD+	Receiver Non-Inverted Data Out. AC Coupled.	
14	VeeR	Receiver Ground (Common with Transmitter Ground).	1
15	VccR	Receiver Power Supply.	
16	VccT	Transmitter Power Supply.	
17	VeeT	Transmitter Ground (Common with Receiver Ground).	1
18	TD+	Transmitter Non-Inverted Data In. AC Coupled.	
19	TD-	Transmitter Inverted Data In. AC Coupled.	
20	VeeT	Transmitter Ground (Common with Receiver Ground).	1

Notes:

1. The circuit ground is isolated from the chassis ground.
2. Disabled: TDIS>2V or open, enabled: TDIS<0.8V.
3. Should be pulled up with 4.7kΩ to 10kΩ on the host board to a voltage between 2V and 3.6V.
4. LOS is an open collector output.

Electrical Pad Layout

Top of Board

Pad	Signal
20	V_EET
19	TD-
18	TD+
17	V_EET
16	V_CCT
15	V_CCR
14	V_EER
13	RD+
12	RD-
11	V_EER

Bottom of Board

Pad	Signal
1	V_EET
2	TX_FAULT
3	TX_DISABLE
4	SDA
5	SCL
6	MOD_ABS
7	RS0
8	LOS
9	RS1
10	V_EER

Top

Bottom
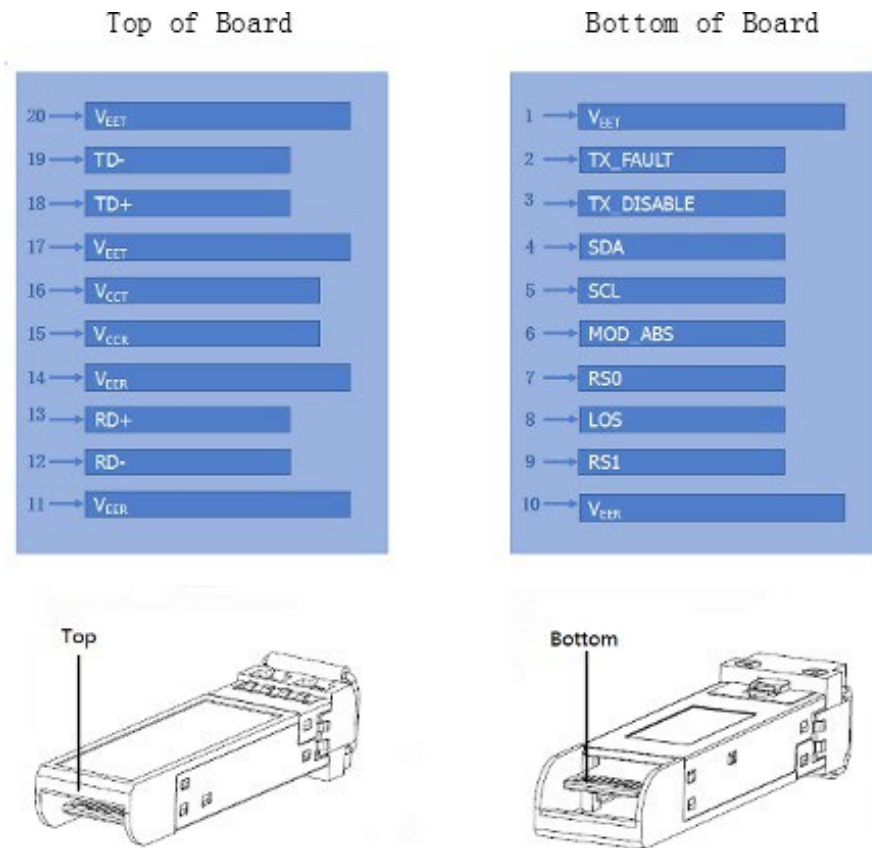
Block Diagram of Transceiver

Mechanical Specifications

56.2

11.0

13.4±0.1

36.5

13.4±0.1

12.98

Rx Tx

6.25

3.53

8.5±0.1

45.18±0.2

LATCHED

9.2±0.1

35.99±0.3

43.35±0.15

UNLATCHED