

AT-SPBD10-13-OPC

Allied Telesis® AT-SPBD10-13 Compatible TAA 1000Base-BX SFP Transceiver (SMF, 1310nmTx/1490nmRx, 10km, LC, DOM)

Features

- INF-8074 and SFF-8472 Compliance
- Simplex LC Connector
- Fabry Perot transmitter and PIN receiver
- Single-mode Fiber
- Commercial Temperature 0 to 70 Celsius
- Hot Pluggable
- Metal with Lower EMI
- Excellent ESD Protection
- RoHS Compliant and Lead Free



Applications:

- 1000Base-BX Ethernet
- 1x Fibre Channel
- Access (FTTx) and Enterprise

Product Description

This Allied Telesis® AT-SPBD10-13 compatible SFP transceiver provides 1000Base-BX throughput up to 10km over single-mode fiber (SMF) using a wavelength of 1310nmTx/1490nmRx via an LC connector. This bidirectional unit must be used with another transceiver or network appliance of complementing wavelengths. It can operate at temperatures between 0 and 70C. Our transceiver is built to meet or exceed OEM specifications and is guaranteed to be 100% compatible with Allied Telesis®. It has been programmed, uniquely serialized, and tested for data-traffic and application to ensure that it will initialize and perform identically. All of our transceivers comply with Multi-Source Agreement (MSA) standards to provide seamless network integration. Additional product features include Digital Optical Monitoring (DOM) support which allows access to real-time operating parameters. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Maximum Supply Voltage	V _{CC}	-0.5	4.0	V
Storage Temperature	T _S	-40	85	°C
Operating Case Temperature	T _C	-5	70	°C
Operating Humidity	RH	5	85	%
Receiver Power	R _{MAX}		-3	dBm
Maximum Bitrate	B _{max}		1.25	Gbps

Electrical Characteristics (TOP=25°C, V_{CC}=3.3Volts)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Power Supply Voltage	V _{CC}	3.15	3.30	3.43	V	
Power Supply Current	I _{CC}			303	mA	
Power Consumption	P _{DISS}			1	W	
Transmitter						
Differential data input swing	V _{in,pp}	120		850	mV	
Input differential impedance	Z _{in}	80	100	120	Ω	
Receiver						
Differential data output swing	V _{out, pp}	300		850	mV	
Output differential impedance	Z _{in}	80	100	120	Ω	

Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Transmitter						
Optical Power (average)	P _{AVE}	-9		-3	dBm	1
Optical Extinction Ratio	ER	9			dB	
Optical Wavelength	Tλ	1260	1310	1360	nm	
Insertion loss	IL		1.2			
Receiver						
Receiver Sensitivity (average)	R _{AVE}			-19.5	dBm	3
Receiver overload	P _{max}	-3			dBm	4
Receiver wavelength	Rλ	1480		1500	nm	

Notes:

1. Coupled into a Single-mode fibre
2. Per IEEE 802.3ah specification
3. Average power, back-to-back, @1.25Gbps, BER 1E-12, PRBS 231-1.
4. Exceeding the Receiver overload can physically damage the module. Please use appropriate attenuation.

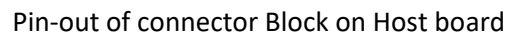
Pin Descriptions

Pin	Symbol	Name/Descriptions	Ref.
1	VeeT	Transmitter Ground (Common with Receiver Ground).	1
2	TX Fault	Transmitter Fault. LVTTTL-O	2
3	TX Disable	Transmitter Disable. Laser output disabled on high or open. LVTT-I.	3
4	SDA	2-Wire Serial Interface Data Line (Same as MOD-DEF2 in INF-8074i). LVTTTL-I/O.	
5	SCL	2-Wire Serial Interface Data Line (Same as MOD-DEF2 in INF-8074i). LVTTTL-I.	
6	MOD_ABS	Module Absent, Connect to VeeT or VeeR in Module.	4
7	RS0	Rate Select 0. Not used	5
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation. LVTTTL-O.	2
9	RS1	Rate Select 1. Not used	5
10	VeeR	Receiver Ground (Common with Transmitter Ground).	1
11	VeeR	Receiver Ground (Common with Transmitter Ground).	1
12	RD-	Receiver Inverted DATA out. AC Coupled. CML-O.	
13	RD+	Receiver Non-inverted DATA out. AC Coupled. CML-O.	
14	VeeR	Receiver Ground (Common with Transmitter Ground).	1
15	VccR	Receiver Power Supply.	
16	VccT	Transmitter Power Supply.	
17	VeeT	Transmitter Ground (Common with Receiver Ground).	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled. CML-I.	
19	TD-	Transmitter Inverted DATA in. AC Coupled. CML-O.	
20	VeeT	Transmitter Ground (Common with Receiver Ground).	1

Notes:

1. The module signal ground contacts, VeeR and VeeT, should be isolated from the module case.
2. This contact is an open collector/drain output and should be pulled up to the Vcc_Host with resistor in the range 4.7KΩ to 10KΩ. Pull ups can be connected to one or several power supplies, however the host board design shall ensure that no module contract has voltage exceeding module VccT/R +0.5.V.
3. Tx_Disable is an input contact with a 4.7KΩ to 10KΩ pull-up resistor to VccT inside module.
4. Mod_ABS is connected to VeeT or VeeR in the SFP+ module. The host may pull the contract up to

5. Internally pulled down per SFF-8431



The schematic diagram illustrates the internal architecture and pin connections of the SerDes IC. Key components and connections include:

- LD Driver:** Connected to the Tx data lines (TD+, TD-) through a 100Ω resistor.
- POST Amp:** Connected to the Rx data lines (RD+, RD-) through a 100Ω resistor.
- EEPROM:** Connected to the MOD_AB3, SCL, and SDA pins.
- Control Pins:**
 - Tx_DISABLE (3):** Connected to the Tx_DISABLE pin of the SerDes IC.
 - Tx_FAULT (2):** Connected to the Tx_FAULT pin of the SerDes IC.
 - Tx_Rate_Select (9):** Connected to the Tx_Rate_Select pin of the SerDes IC.
 - Rx_Rate_Select (7):** Connected to the Rx_Rate_Select pin of the SerDes IC.
 - MOD_AB3 (6):** Connected to the MOD_AB3 pin of the SerDes IC.
 - SCL (5):** Connected to the SCL pin of the SerDes IC.
 - SDA (4):** Connected to the SDA pin of the SerDes IC.
- Power Supply:**
 - VccT (16):** Connected to the VccT pin of the SerDes IC.
 - VccR (15):** Connected to the VccR pin of the SerDes IC.
- Data Lines:**
 - TD+ (18):** Connected to the TD+ pin of the SerDes IC.
 - TD- (19):** Connected to the TD- pin of the SerDes IC.
 - RD+ (13):** Connected to the RD+ pin of the SerDes IC.
 - RD- (12):** Connected to the RD- pin of the SerDes IC.
- Other Pins:**
 - Loss of Signal (8):** Connected to the Loss of Signal pin of the SerDes IC.
 - Module Detect (10, 11, 14):** Connected to the Module Detect pin of the SerDes IC.

Mechanical Specifications

Small Form Factor Pluggable (SFP) transceivers are compatible with the dimensions defined by the SFP Multi-Sourcing Agreement (MSA).



EEPROM Information

EEPROM memory map specific data field description is as below:

2 wire address 1010000X (A0h)		2 wire address 1010001X (A2h)	
0	Serial ID Defined by SFP MSA (96 bytes)	0	Alarm and Warning Thresholds (56 bytes)
95		55	Cal Constants (40 bytes)
127	Vendor Specific (32 bytes)	95	Real Time Diagnostic Interface (24 bytes)
	Reserved, SFF8079 (128 bytes)	119	Vendor Specific (8 bytes)
255		127	User Writable EEPROM (120 bytes)
		247	Vendor Specific (8 bytes)
		255	