OPTIO CONNECT

QSFP28-100GB-4WDM40-AR-OPC

Arista Networks® Compatible TAA 100GBase-4WDM-40 QSFP28 Transceiver (SMF, 1295nm to 1309nm, 40km, LC, DOM)

Features

- QSFP28 MSA compliant
- Supports 103Gbps
- Single 3.3V Power Supply and Power dissipation < 4.8W
- Up to 40km over SMF with FEC on host
- Operating case temperature: 0 to 70C
- Four 25Gbps DFB-based LAN-WDM transmitter
- PIN and TIA array on the receiver side
- 4x25G electrical interface
- Duplex LC receptacles
- I2C interface with integrated Digital Diagnostic Monitoring
- RoHS Compliant and Lead Free

Applications:

- 100GBase Ethernet
- Access and Enterprise

Product Description

This Arista Networks® compatible QSFP28 transceiver provides 100GBase-4WDM-40 throughput up to 40km over single-mode fiber (SMF) using a wavelength of 1295nm to 1309nm via an LC connector. It can operate at temperatures between 0 and 70C. Our transceiver is built to meet or exceed OEM specifications and is guaranteed to be 100% compatible with Arista Networks®. It has been programmed, uniquely serialized, and tested for data-traffic and application to ensure that it will initialize and perform identically. All of our transceivers comply with Multi-Source Agreement (MSA) standards to provide seamless network integration. Additional product features include Digital Optical Monitoring (DOM) support which allows access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

OptioConnect's transceivers are RoHS compliant and lead-free.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Maximum Supply Voltage	Vcc	-0.5	3.6	V
Storage Temperature	TS	-40	+85	°C
Operating Case Temperature	Tc	0	70	°C
Operating Humidity	RH	5	85	%

Electrical Characteristics

Parameter	Test Point	Min.	Typ.	Max.	Unit	Notes
Power Supply Voltage	Vcc	3.135	3.3	3.465	V	
Power Dissipation	PD			5	W	
Transmitter						
Differential data input swing per lane				900	mv_{p-p}	
Input Impedance (Differential)	Zin			10	%	
Stressed Input Parameters						
Eye width		0.46			UI	
Applied pk-pk sinusoidal jitter		IEEE 802.3bm Table 88-13				
Eye height		95			mv	
DC common mode voltage		-350		2850	mv	
Receiver						
Differential output amplitude		200		900	mv_{p-p}	
Output Impedance (Differential)	Zout			10	%	
Eye width		0.57			UI	
Eye height differential		228			mv	
Vertical eye closure				5.5	dB	

Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Transmitter						
Signaling Speed per Lane	BR_{AVE}		25.78		Gbps	
Data Rate Variation		-100		+100	ppm	
Lane_0 Center Wavelength	λ_{C0}	1294.53	1295.56	1296.59	nm	
Lane_1 Center Wavelength	λ_{C1}	1299.02	1300.05	1301.09	nm	
Lane_2 Center Wavelength	λ_{C2}	1303.54	1304.58	1305.63	nm	
Lane_3 Center Wavelength	λ_{C3}	1308.09	1309.14	1310.19	nm	
Total Average Output Power	Po			12.5	dBm	
Average Launch Power per Lane	Peach	-2.5		6.5	dBm	1
Average launch power of OFF transmitter per lane				-30	dBm	
Side-mode suppression ratio	$SMSR_{min}$	30			dB	
Optical modulation amplitude	Poma	0.5		6.5	dBm	2
Difference in launch power between any two lanes (OMA)				4	dB	
Optical Return Loss Tolerance		20			dB	
Transmitter reflectance				-26		3
Extinction Ratio	ER	4.5			dB	4
Transmitter Eye Mask Definition {X1, X2, X3, Y1, Y2, Y3}		{0.31, 0.4, 0.45, 0.34, 0.38, 0.4}				4
Receiver						
Signaling Speed per Lane	BR_{AVE}		25.78		Gbps	
Data Rate Variation		-100		+100	ppm	
Damage threshold per Lane	Rdam	-2.5			dBm	
Lane_0 Center Wavelength	λ_{C0}	1294.53	1295.56	1296.59	nm	
Lane_1 Center Wavelength	λ_{C1}	1299.02	1300.05	1301.09	nm	
Lane_2 Center Wavelength	λ_{C2}	1303.54	1304.58	1305.63	nm	
Lane_3 Center Wavelength	λ_{C3}	1308.09	1309.14	1310.19	nm	
Average Receive Power per Lane	Rxpow	-20.5		-3.5	dBm	5
Receive Sensitivity in OMA per Lane	Rxsens			-18.5	dBm	6
Stressed Receiver Sensitivity (OMA) per Lane	RX_{SRS}			-16	dBm	7
Optical Return Loss	ORL			-26	dB	
LOS Assert	LOSA	-30			dBm	
LOS De-Assert	LOSD			-20	dBm	
LOS Hysteresis		0.5			dB	
Conditions of Receiver Sensitivity Test (Note 8)						
Vertical Eye Closure Penalty	VECP		2.5		dB	
Stressed Eye J2 Jitter			0.33		UI	

Stressed Eye J4 Jitter			0.48		UI	
SRS Eye Mask Definition {X1, X2, X3, Y1, Y2, Y3}		{0.39, 0.5, 0.5, 0.39, 0.39, 0.4}			

Notes:

1. Average launch power, each lane (min) is informative and not the principal indicator of signal strength. A transmitter with launch power below this value cannot be compliant; however, a value above this does not ensure compliance.
2. Even if the TDP < 1.0dB, the OMA (min) must exceed this value.
3. Transmitter reflectance is defined looking into the transmitter.
4. Eye mask hit ratio is 5E-5.
5. Average launch power, each lane (min) is informative and not the principal indicator of signal strength. A received power below this value cannot be compliant; however, a value above this does not ensure compliance.
6. Receiver sensitivity (OMA), each lane (max) at 5E-5 BER is a normative specification.
7. Measured with conformance test signal at TP3 for BER = $5*10^{-5}$.
8. Vertical eye closure penalty, stressed eye J2 Jitter, stressed eye J4 Jitter, and SRS eye mask definition are test conditions for measuring stressed receiver sensitivity. They are not characteristics of the receiver.

Electrical Pin-out Details

38	GND
37	TX1n
36	TX1p
35	GND
34	TX3n
33	TX3p
32	GND
31	LPMode
30	Vcc1
29	VccTx
28	IntL
27	ModPrsL
26	GND
25	RX4p
24	RX4n
23	GND
22	RX2p
21	RX2n
20	GND

Card Edge

GND	1
TX2n	2
TX2p	3
GND	4
TX4n	5
TX4p	6
GND	7
ModSelL	8
ResetL	9
VccRx	10
SCL	11
SDA	12
GND	13
RX3p	14
RX3n	15
GND	16
RX1p	17
RX1n	18
GND	19

Top Side
Viewed from Top

Bottom Side
Viewed from Bottom
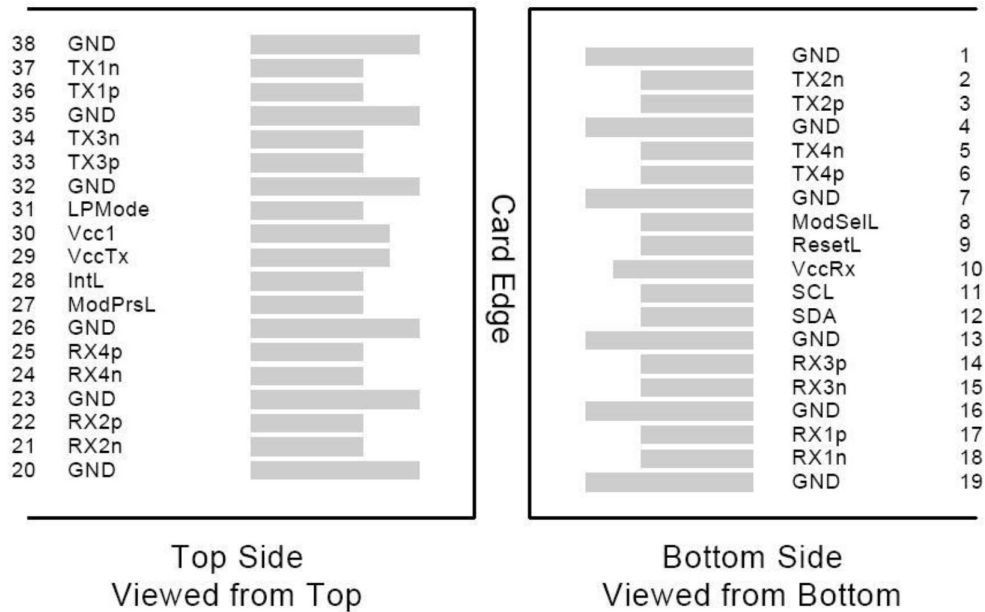
Pin Descriptions

| Pin	Symbol	Name/Descriptions	Ref.
1	GND	Ground	1
2	Tx2n	Transmitter Inverted Data Input	
3	Tx2p	Transmitter Non-Inverted Data output	
4	GND	Ground	1
5	Tx4n	Transmitter Inverted Data Input	
6	Tx4p	Transmitter Non-Inverted Data output	
7	GND	Ground	1
8	ModSelL	Module Select	
9	ResetL	Module Reset	
10	VccRx	+3.3V Power Supply Receiver	2
11	SCL	2-Wire Serial Interface Clock	1
12	SDA	2-Wire Serial Interface Data	
13	GND	Ground	
14	Rx3p	Receiver Non-Inverted Data output	
15	Rx3n	Receiver Inverted Data output	
16	GND	Ground	1
17	Rx1p	Receiver Non-Inverted Data output	
18	Rx1n	Receiver Inverted Data output	
19	GND	Ground	1
20	GND	Ground	1
21	Rx2n	Receiver Inverted Data output	
22	Rx2p	Receiver Non-Inverted Data output	
23	GND	Ground	1
24	Rx4n	Receiver Inverted Data output	1
25	Rx4p	Receiver non-Inverted Data output	
26	GND	Ground	1
27	ModPrsL	Module Present	
28	IntL	Interrupt	
29	VccTx	+3.3V Power Supply Transmitter	2
30	Vccl	+3.3V Power Supply	2
31	LPMode	Low Power Mode	
32	GND	Ground	1
33	Tx3p	Transmitter Non-Inverted Data input	
34	Tx3n	Transmitter Inverted Data output	
35	GND	Ground	1
36	Tx1p	Transmitter Non-Inverted Data input	
37	Tx1n	Transmitter Inverted Data output	
38	GND	Ground	1

Notes:

1. GND is the symbol for signal and supply (power) common for QSPF28 modules. All are common within the QSPF28 module and all module voltages are referenced to this potential unless otherwise noted. Connected these directly to the host board signal common ground plane.

2. VccRx, Vcc1 and VccTx are the receiving and transmission power suppliers and shall be applied concurrently. VccRx, Vcc1 and VccTx may be internally connected within the QSFP28 transceiver module in any combination. The connector pins are each rated for a maximum current of 1000mA.

Mechanical Specifications

Measurement unit: mm
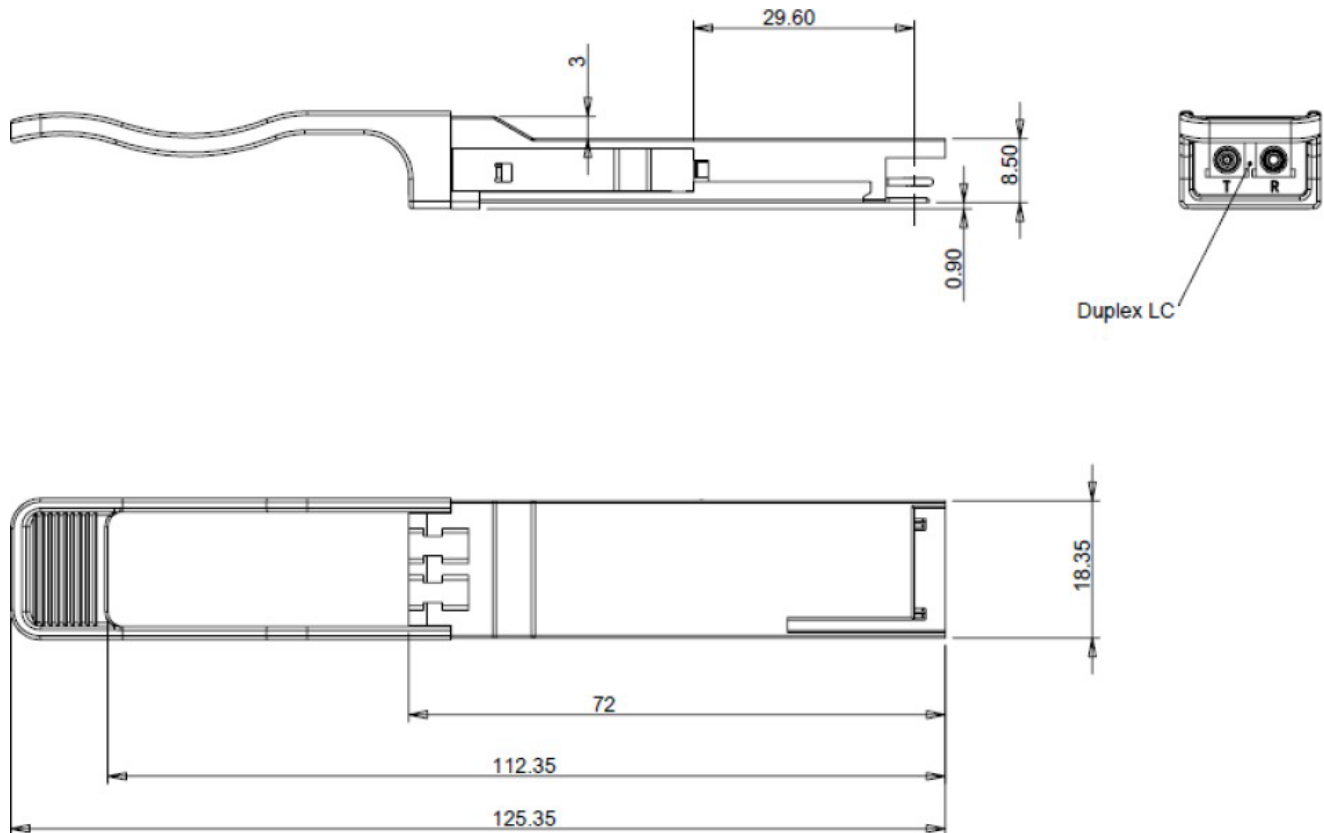
OptioConnect
Innovation for the Future of High-Speed Networking

Who We Are
OptioConnect is reshaping the landscape of communication and high-speed networking through intelligent technology. With a core focus on cutting edge technology, we deliver smarter fiber optic solutions for enterprise networks, data centers, and next-gen telecom infrastructures.

What We Do
At OptioConnect, we fuse advanced engineering with intelligent automation to drive the future of networking. Our AI-integrated solutions are designed to optimize performance and streamline operations with:

- Superior Performance
- Network and traffic optimization
- Intelligent energy management
- Seamless OEM compatibility
- Scalable cost-efficiency

Smarter Networks by Design
Innovation isn't just a goal—it's our process. We embed AI and machine learning across our R&D and product lines, enabling adaptive performance, automated tuning, and faster deployment cycles. The result? Networks that don't just work—they learn, evolve, and outperform.

Our Team
Our engineers, data scientists, and network architects bring decades of experience and a future-focused mindset. We provide hands-on support with intelligent insights that turn complex challenges into simple solutions.

Our Mission
To deliver AI-enhanced connectivity that reduces cost, increases speed, and maximizes efficiency—empowering our partners to operate at the forefront of a rapidly evolving digital world.

Let's Connect
Discover how OptioConnect's intelligent infrastructure solutions can power your network's next leap forward.
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